

OPTICAL LOGIC GATE BASED OPTICAL ROUTER

CROSS REFERENCE TO RELATED APPLICATIONS

This application is a continuation-in-part of Application Serial No. 09/776,052 by Mohammed N. Islam, filed February 2, 2001, entitled "Variable Blazed Grating  
5 Based Signal Processing." Application Serial No. 09/776,052 is related to Application Serial Number 09/776,051, entitled "*Variable Blazed Grating*," filed on February 2, 2001.

10 TECHNICAL FIELD OF THE INVENTION

This invention relates in general to the field of electro-optical systems and more particularly to a system and method capable of using an optical router having one or more all-optical logic gates.

OVERVIEW

The ability to transmit information in the optical domain has greatly enhanced the speed and bandwidth of data communications. Optical communication networks that  
5 transmit information in the optical domain typically require optical-to-electrical and electrical-to-optical conversions as the optical signals are routed through the network. Converting information between the optical and electrical domains can, in most cases, reduce and/or  
10 limit the transmission speed and bandwidth of the optical communication network. In addition, the inability of conventional systems to route the information in the optical domain has restricted the ability of network designers to accomplish data communications solely in the  
15 optical domain.

SUMMARY OF EXAMPLE EMBODIMENTS

In one embodiment, a blazed grating based electro-optic switching system comprises a fiber optic tap. The fiber optic tap operable to receive an optical signal having header information and payload information and to form a first signal copy comprising at least the header information and a second signal copy comprising at least the payload information. The system also comprises an electronic processor operable to receive the first signal copy and to perform electronic processing on the header information. The system further comprises an array of blazed grating based optical switch elements operable to receive the first and second signal copies and to perform an optical switching operation on the first and second signal copies.

In another embodiment, a logic gate capable of being used in an optical processing device comprises at least a first optical amplifier and a second optical amplifier located approximately symmetrically in a Mach Zehnder Interferometer (MZI). The first optical amplifier operable to receive a first data signal and the second optical amplifier operable to receive a second data signal. In one particular embodiment, the first data signal and the second data signal are received substantially simultaneously. The logic gate further comprises a light source coupled to the Mach Zehnder Interferometer and operable to generate a clock signal, wherein the clock signal traverses the first optical amplifier in a direction that is counter to a direction that the first data signal traverses the first optical amplifier.

In yet another embodiment, a switching element capable of being used in an optical processing device

comprises an optical signal separator operable to separate a multiple wavelength optical signal into one or more optical signal wavelengths. The switching element also comprises a plurality of semiconductor optical amplifiers located on a single semiconductor substrate. The plurality of semiconductor optical amplifiers operable to perform an optical switching operation on at least one of the optical signal wavelengths. The switching element further comprises controller operable to generate a control signal that affects the optical switching operation performed by one or more of the plurality of semiconductor optical amplifiers.

In still another embodiment, an logic gate capable of being used in an optical router comprises a plurality of semiconductor optical amplifiers located in an interferometer. At least some of the plurality of semiconductor optical amplifiers operable to receive at least one data signal. In one particular embodiment, at least one of the plurality of semiconductor optical amplifiers operates at transparency. The logic gate also comprises a light source coupled to the plurality of optical amplifiers and operable to generate a clock signal.

In another embodiments, an optical switching system comprises a fiber optic tap operable to receive an optical signal having at least one packet label and packet data. The tap also operable to separate the optical signal into a first signal copy and a second signal copy comprising at least packet label. The switching system also comprises a first all-optical processing device operable to receive the second signal copy and to perform optical processing on the at least one packet label. The switching system further comprises

a second all-optical processing device operable to receive the first signal copy and the processed second signal copy, and to perform an optical switching operation on the first signal copy. In one particular  
5 embodiment, at least one of the first and second all-optical processing devices comprises a plurality of semiconductor optical amplifiers located approximately symmetrically in an interferometer.

In yet another embodiment, a regenerative device  
10 capable of regenerating one or more optical signals comprises an optical signal separator operable to separate a multiple wavelength optical signal into one or more optical signal wavelengths. The device further comprises a plurality of semiconductor optical amplifiers  
15 located on a single semiconductor substrate. The plurality of semiconductor optical amplifiers collectively operable to perform an optical switching operation on at least one of the plurality of optical signal wavelengths. The device also comprises a light  
20 source coupled to the plurality of optical amplifiers and operable to generate at least a modulated clock signal.

Depending on the specific features implemented, particular embodiments of the present invention may exhibit some, none, or all of the following technical  
25 advantages. For example, various embodiments may be capable of minimizing and/or avoiding contention within the communication network and/or the optical data router. Some embodiments may be amenable to semiconductor chip level integration. Other embodiments may be capable of  
30 maintaining one or more packet data associated with an optical signal in the optical domain.

Other technical advantages will be readily apparent to one skilled in the art from the following figures,

description and claims. Moreover, while specific advantages have been enumerated, various embodiments may include all, some or none of the enumerated advantages.

BRIEF DESCRIPTION OF THE DRAWINGS

For a more complete understanding of the present invention, and for further features and advantages thereof, reference is now made to the following  
5 description taken in conjunction with the accompanying drawings, in which:

FIGURES 1a-1c are block diagrams illustrating greatly enlarged cross-section views of various exemplary embodiments of blazed grating-based apparatus operable to  
10 facilitate high speed optical signal processing;

FIGURES 2a and 2b illustrate planar views of one particular embodiment of an apparatus operable to facilitate high speed optical signal processing;

FIGURES 3a-c are cross-sectional and planar diagrams  
15 showing one example of a blazed grating device;

FIGURES 4a-c are cross-sectional and planar diagrams showing another example of a blazed grating device;

FIGURES 5a-c are cross-sectional and planar diagrams showing still another example of a blazed grating device;

20 FIGURES 6a-c are cross-sectional and planar diagrams showing yet another example of a blazed grating device;

FIGURES 7a and 7b illustrate blazed grating based variable optical attenuators;

FIGURE 8 is a block diagram showing a combination of  
25 a variable blazed grating and an optical circulator;

FIGURES 9a-9b are block diagrams illustrating examples of blazed grating based 1x2 optical switches;

FIGURES 10a-10b are block diagrams illustrating various modes of operation of a blazed grating based 2x2  
30 optical switch;

FIGURES 11a-11h are block diagrams illustrating examples of various embodiments of blazed grating based optical add/drop multiplexers;

FIGURE 12 is a block diagram showing one example of a novel system for facilitating multiple-wavelength signal processing;

FIGURES 13a-13b are block diagrams illustrating  
5 examples of various embodiments of a blazed grating based optical gain equalizer;

FIGURES 14a and 14b are block diagrams illustrating example embodiments of blazed grating based wavelength division optical add/drop multiplexer;

10 FIGURE 15 is a block diagram of an exemplary blazed grating based electro-optic router;

FIGURE 16 is a flow chart illustrating one example of a method of optical signal processing using a blazed grating based apparatus;

15 FIGURE 17 is a block diagram of one embodiment of an optical data router;

FIGURE 18 is a block diagram of one embodiment of an input interface;

FIGURE 19 is a block diagram of one embodiment of a  
20 self-sync module;

FIGURE 20 is a block diagram of one embodiment of an all-optical label swapping module;

FIGURE 21 is a block diagram of one embodiment of a replication module implementing a multi-mode interference  
25 coupler;

FIGURES 22A through 22D are block diagrams of example embodiments of all-optical logic gates for bit level processing;

FIGURE 23 is a block diagram of one example of an  
30 optical switching element;

FIGURE 24 is a block diagram of one embodiment of an electro-optic core router node;



FIGURE 25 is a block diagram of one embodiment of an all-optical core router node;

FIGURE 26 is a block diagram of one embodiment of an all-optical header processing device;

5       FIGURE 27 is a block diagram of one embodiment of a packet switch;

FIGURE 28 is a block diagram of one embodiment of a management node;

10       FIGURE 29 is a block diagram of one embodiment of an all-optical error check device;

FIGURE 30 is a block diagram of one embodiment of an output interface; and

15       FIGURE 31 is a block diagram of one embodiment of an optical communication network capable of implementing one or more core optical data routers.

DETAILED DESCRIPTION OF EXAMPLE EMBODIMENTS

Generally, a variable blazed grating device is an element having a diffraction grating that can be selectively displaced relative to an incoming optical  
5 signal, with the result that the majority of the diffracted portions of the optical signal are communicated in one direction. One aspect of one embodiment of the present invention relates to a novel configuration of a variable blazed grating device.

10 FIGURE 1a shows a cross-section view of one exemplary embodiment of a variable blazed grating-based apparatus 100 operable to facilitate high speed optical signal processing. Throughout this document, the term "signal processing" includes attenuation, switching,  
15 phase shifting, or any other manipulation of one or more optical signals.

In this example, apparatus 100 includes a substrate 12 and a plurality of strips 14 disposed outwardly from substrate 12. In a particular embodiment, substrate 12  
20 comprises a semiconductor substrate formed, for example, from silicon. Other materials could be used for substrate 12 without departing from the scope of the invention.

Each strip 14 has a width ( $W_s$ ), and is separated from  
25 adjacent strips by a distance ( $d$ ). The width ( $W_s$ ) and the distance ( $d$ ) define a periodicity associated with the strips. Multiple strips 14 are operable to receive a single input optical signal 20 having a beam width ( $W_b$ ). Strips 14 are sized and spaced from one another in a  
30 manner to ensure that the width ( $W_b$ ) of received optical beam 20 covers at least two strips 14. In this example, strips 14 residing at position 14' are spaced from substrate 12 by a distance 16. Although strips 14 are

shown as generally rectangular in shape, any shape can be used consistent with the invention. In addition, although strips 14 are shown as having a constant width ( $W_s$ ), that measurement could vary between strips, or even  
5 along the same strip 14.

As one particular non-limiting example of particular dimensions, the width of optical beam 20 may comprise approximately 21,000 nanometers, while each strip 14 comprises a width of approximately 3,000 nanometers (3  
10 microns) and is spaced from adjacent strips 14 by approximately 600 nanometers. In this particular example, strips 14 are spaced from substrate 12 by approximately 2000 nanometers. These dimensions are provided for illustrative purposes only. Other device  
15 dimensions and configurations could be used without departing from the scope of the invention.

At least outer surface 15 of each strip 14 comprises an at least partially reflective material. It is not necessary for surface 15 to be completely or even mostly  
20 reflective. Of course, the more reflective the material or materials comprising outer surface 15, the less lossy the device will be. Reflective surface 15 may comprise the outer surface of strips 14 where strips 14 are formed from a reflective material. For example, strips 14 may  
25 be formed from a metal, such as aluminum, chromium, or gold. As a further example, strips 14 could be formed from polysilicon formed at a thickness sufficient to render the strips at least partially reflective of at least the wavelengths being processed by apparatus 100.  
30 Other materials could be used to form strips 14 without departing from the scope of the invention.

In another embodiment, reflective surface 15 may comprise a layer of reflective material disposed

outwardly from another layer of strip 14. For example, strips 14 could be formed from a material, such as, silicon nitride, and a layer of partially reflective material 15 could be formed outwardly from strip 14. In that embodiment, the layer of material supporting layer 15 may, but need not be reflective of the incident signals.

FIGURE 1b illustrates one example of operation of apparatus 100. In this example, strips 14 receive optical input beam 20 at an angle normal to the surface of strips 14 at position 14.' Strips 14 at position 14' (shown in dotted lines) show apparatus 100 operating in "reflection mode," where strips 14 operate to reflect input optical beam 20 as reflected signal 24. In this case, because input beam 20 is oriented normally to the surfaces of strips 14, reflected beam 24 is communicated back in the same direction from which input beam 20 originated. As will be discussed below, non-normal input angles could also be used.

Strips at positions 14" (shown in solid lines) depict strips 14 during a second mode of operation, "diffraction mode." In diffraction mode, strips 14 are each rotated by approximately a blaze angle  $\theta$  from the original position of strips 14. In a particular embodiment, strips 14 can obtain a maximum blaze angle that is greater than two degrees. Implementing a design that facilitates a wide range of strip rotation provides significant advantages over other approaches by, for example, providing flexibility in system configuration. Input optical beam 20 impinges on surfaces 15 of strips 14. In this example, a first portion of input optical beam 20 impinges on strip 14a, while a second portion of beam 20 impinges on strip 14b, which is adjacent to strip

14a. While beam 20 may experience some scattering, because of the rotation of strips 14 to position 14'', the majority of the diffracted portions of input beam 20 are directed in one direction, as illustrated (at least in part) by output rays 30 and 32.

Output ray 30 represents the portion of input beam 20 reflected by strip 14a at position 14'' and output beam 32 represents the portion of input beam 20 that is reflected by strip 14b at position 14''. Although FIGURE 1b shows just two output rays 30 and 32, it should be appreciated that any strips 14' that receive a portion of input beam 20 will reflect an output portion in the direction indicated by arrows 30 and 32.

Because output rays 30 and 32 result from diffractions from surfaces laterally offset from one another and positioned at an angle to input beam 20, output rays 30 and 32 experience a relative difference ( $d_{\text{path}}$ ) in their path lengths. This path length difference ( $d_{\text{path}}$ ) results in a phase difference between the output rays. For a given wavelength and strip periodicity, apparatus 100 can introduce any level of phase difference between output rays by varying the angle THETA by which the strips 14 are rotated. When using a normal incident input beam 20, the diffracted output signal comprising a combination of diffracted rays, such as 30 and 32, is at a maximum when the path difference  $d_{\text{path}}$  corresponds to one wavelength (or an integral multiple of wavelengths) of beam 20. Other path differences  $d_{\text{path}}$  result in an attenuation of the output signal compared to the maximum condition.

FIGURE 1c illustrates another example of operation of apparatus 100. In this example, strips 14 receive optical input beam 20 at a non-normal angle PHI. In this

particular example, the angle of incidence  $\Phi$  of input beam 20 is equal to the angle of diffraction of output rays 30 and 32. As a result, the diffracted output rays travel back in the same direction as input beam 20. This  
5 condition is commonly referred to as the Littrow case.

In this embodiment, apparatus 100 operates in reflection mode when strips 14 reside at positions 14'. In that mode, input beam 20 impinges on strips 14 at angle  $\Phi$  and is reflected as shown by output beam  
10 portion 24 at an angle of  $180^\circ$  minus  $\Phi$ . In diffraction mode, strips 14 reside at positions 14" at an angle  $\Theta$  from position 14'. In this case, grazing angle  $\Phi$  is selected to result in a diffraction angle that is approximately equal to the incident angle (ninety  
15 degrees minus  $\Phi$ ), resulting in input beam 20 being diffracted back in approximately the same direction as the origin of input beam 20, as shown by output rays 30 and 32.

Again, output rays 30 and 32 experience a relative  
20 path difference, which results in a relative phase shift between the signal portions. In this case, the beam portions experience a path difference before and after impinging on strips 14. As a result, the parallel distance between strips 14 at positions 14" equals one  
25 half of the resulting path difference. Operating blazed grating devices using non-normal incident angles -- such as in the Littrow condition -- can provide an advantage of facilitating the necessary phase shift between output rays while requiring only a portion of the strip rotation  
30 otherwise required. This results in less required drive voltage and more economical operation. Alternatively, the same drive voltage can be used with more rigid strips, facilitating faster device operation.

Previous systems using variable blazed gratings either implemented continuous deformable membranes or implemented multiple-piece membranes requiring very wide slats (typically ranging in width from nearly 60,000  
5 nanometers (60 microns) to over 80,000 nanometers (80 microns)). The systems using slats require wide slats due to the high power of the optical signals being redirected and, as a consequence, are severely limited in their ability to rotate to change the blaze angle  
10 (typically limited to a maximum blaze angle of approximately 1.8 degrees).

One aspect of the present invention uses narrow strips, no wider than 40,000 nanometers (40 microns), to ensure greater blaze angle capabilities, lower drive  
15 voltage, and faster operation, while maintaining good contrast ratios and high output beam resolution as compared to other approaches.

The maximum switching speed, the minimum required drive voltages, and the maximum attainable blaze angle  
20 depend, at least in part, on the width of strips 14 and the ratio of that width to the space 16 separating strips 14 from substrate 12 (or another layer disposed outwardly from substrate 12). These devices operate by introducing a path difference  $d_{\text{path}}$  between diffracted signal portions  
25 to create a desired phase shift between the portions. The path difference is typically some fraction of a wavelength of the signal being processed. In a particular embodiment, the spacing 16 can be selected to facilitate a maximum strip displacement of approximately  
30 one wavelength of the signal being processed. This facilitates introduction of any path difference up to a full wavelength of the signal.

For a given wavelength signal, the strip width and maximum strip displacement can be selected to provide any desired blaze angle. Typical telecommunication signals have wavelengths of approximately 1400 - 1600 nanometers. The following table shows example values of strip widths that can be useful using, for example, a 1400 nanometer maximum strip displacement.

Assuming maximum strip displacement = 1400 nanometers,

10	<u>Maximum Blaze Angle</u>	<u>Strip Width</u>
	2 degrees	40 microns
	5 degrees	16 microns
	7.5 degrees	10.6 microns
	10 degrees	7.9 microns
15	15 degrees	5.3 microns
	20 degrees	3.8 microns
	25 degrees	3.0 microns
	30 degrees	2.4 microns
20	45 degrees	1.4 microns

The foregoing table is intended to provide example dimensions only. Other device configurations could be used using other spacings 16, strip widths, and/or maximum blaze angles.

Various aspects of the present invention provide advantages over previous approaches by providing blazed grating having a number of narrow strips. Narrow strips facilitate larger maximum blaze angles for a given distance 16 from substrate 12. This provides increased flexibility in component configuration for applications using these devices. In addition, the narrow width of strips 14 allows the strips to be placed closer to substrate 12 while maintaining flexibility in the attainable blaze angles. Placing the strips closer to substrate 12 provides an advantage of reducing the energy needed to rotate the strips (whether in the form of an



electrostatic force between the strips and the substrate or in the form of a forced caused by thermal expansion of a material between the substrate and the strips).

Moreover, reducing the width of strips 14  
5 facilitates faster device operation and lower drive voltages. Using a number of narrow strips 14 further provides an advantage of increasing the resolution of the diffracted output of the device. One aspect of the invention recognizes that the resolution of the output  
10 signal comprising the majority of the diffracted portions of input beam 20 increases as the number of strips illuminated by input beam 20 increases. This aspect of the invention, therefore, facilitates enhancing the resolution of the diffracted output by sizing strips 14  
15 so that input beam 20 illuminates a number of strips 14.

As will be discussed in more detail below, the ability of blazed grating apparatus 100 to selectively attenuate and/or switch optical input beams quickly, while requiring a low drive voltage and maintaining a  
20 good contrast ratio renders apparatus 100 useful in a myriad of applications, such as variable attenuators, gain equalizers, optical switches, and optical add/drop multiplexers, to name a few.

FIGURES 2a and 2b illustrate planar views of one  
25 particular embodiment of apparatus 100. As shown in these figures, strips 14 can be anchored to substrate 12 at anchor points 17. In this embodiment, anchor points have a width ( $W_a$ ) that is smaller than the width ( $W_s$ ) of at least a portion of strip 14. In this manner, strips  
30 14 operate to undergo a partial rotation as shown in FIGURE 2b when a force ( $F$ ) is applied to one side of each strip 14. Apparatus 100 controls the grating angle  $\theta$  by applying a selected level of force ( $F$ ) in selected

locations of strips 14. Other methods of anchoring strips 14 to facilitate rotation could be used consistent with the invention.

FIGURES 3a-c are cross-sectional and planar diagrams showing one example of a mechanism operable to generate and apply a force (F) to cause a partial rotation of strips 14 in a blazed grating apparatus 105. The illustrated embodiment assumes that force (F) is an electrostatic force generated by a voltage differential between an inner conductive layer and at least a portion of grating 14. Alternatively, force (F) could comprise a force pushing up on strips 14 and created by applying a heat source to the inner conductive layer causing that layer to physically expand and push up on a portion of strip 14, causing strip 14 to rotate. In that embodiment, the inner conductive layer could be considerably thicker than the embodiment shown in FIGURES 3a-3c.

Apparatus 105 is similar in structure and operation to apparatus 100 shown in FIGURES 1-2. In one particular embodiment, strips 14 can be constructed from an at least substantially conductive material. For example, strips 14 may be formed from a metal such as aluminum, gold, or titanium, or may be formed from polysilicon. Where strips 14 are formed from polysilicon, the strips may, if desired, be doped to achieve additional conductivity.

The illustrated embodiment of apparatus 105 includes an inner conductive layer 40, which in this case takes the form of a plurality of elongated conductors, each disposed inwardly from one side of (perhaps along an edge 38 of) strip 14 that is desired to be pulled toward substrate 12. Each conductor of inner conductive layer 40 may be formed, for example, from a metal such as

aluminum, chromium, or gold. Other at least substantially conductive materials could be used without departing from the scope of the invention. Although this example assumes creation of an electrostatic force (F),  
5 similar results could be obtained by thermally expanding the inner conductive layer to cause a rotation in strip 14.

By applying a voltage difference between conductors 40 and strips 14 desired to be rotated, an electrostatic  
10 force (F) is generated that acts to pull edge 38 of strip 14 toward conductor 40. This, in turn, operates to partially rotate strip 14 as shown in FIGURE 3c. The voltage difference between strips 14 and conductors 40 may be established, for example, by grounding strips 14  
15 while applying a voltage to conductors 40, grounding conductors 40 while applying a voltage to strips 14, or applying a differential voltage between strips 14 and conductors 40. In the illustrated example, a common voltage (or ground) is applied to all strips 14.  
20 Alternatively, selected strips 14 could be rotated while others remain stationary.

FIGURES 4a-4c are cross-sectional and planar diagrams showing another example of a mechanism operable to generate and apply a force (F) to cause a partial  
25 rotation of strips 114 in an apparatus 110. In this example, strips 114 are similar in function to strips 14 of FIGURES 1-3. However, in this case, strips 114 each comprise a multi-layered structure. In this example, each strip 114 includes a layer of insulating material  
30 113. Insulating material 113 may comprise, for example, silicon nitride, oxide, or any other substantially insulating material.

In the illustrated embodiment, a layer 117 of material that is at least partially reflective is formed outwardly from layer 113 of insulating material. For example, layer 117 may comprise a metal or doped polysilicon. Layer 117 includes a first portion 115 and a second portion 118. Portions 115 and 118 are electrically separated, in this example by a channel 116. Channel 116 may comprise an open channel, or may be filled with an insulating material, such as oxide, or silicon nitride.

Portions 115 and 118 may be formed, for example by first forming a continuous layer 117 of material, and then etching channel 116 to form the first and second portions on either side of channel 116. Alternatively, portions 115 and 118 may be formed by first masking channel 116, and then forming first and second portions 115 and 118 14 on either side of channel 116. First and second portions 115 and 118 may, but need not be formed from the same material.

Blazed grating apparatus 110, like apparatus 105, also includes an inner conductive layer. While apparatus 105 includes an inner conductive layer 40 in the form of a plurality of elongated conductors, apparatus 110 comprises an inner conductive layer 140 in the form of a continuous conductor layer disposed outwardly from substrate 12. In an alternative embodiment, inner conductive layer 140 could comprise substrate 12, where substrate 12 comprises a substantially conductive material, such as metal or doped polysilicon. Inner conductive layer 40 may comprise any configuration of at least substantially conductive material operable to cause a partial rotation of some or all of strips 14.

As illustrated, for example, by FIGURES 4b and 4c, first portion 115 of reflective conducting layer 117 is at least substantially electrically isolated from inner conductive layer 140. Second portion 118 of reflective  
5 conducting layer 117, however, is electrically coupled to inner conductive layer 140, in this case, by physically contacting that layer at region 119. This construction, and others similar to it, maintain a partially reflective outer surface of strips 114, while facilitating creation  
10 of a voltage differential between inner conductive layer 140 and only the edges of strips 114 that carry first portions 115 of conductive reflecting layer 117. This, in turn, facilitates partial rotation of strips 114 upon application of a differential voltage between inner  
15 conductive layer 140 and first portions 115 of strips 114.

In operation, blazed grating apparatus 110 receives optical input beam 20, in this example, at a normal angle of incidence. Although this description assumes a normal  
20 angle of incidence for optical beam 20, non-normal incident angles could be used without departing from the scope of the invention. In reflection mode (as indicated in FIGURE 4a by dashed lines) apparatus 110 substantially reflects input optical beam 20 back in the same direction  
25 as output beam 24. In diffraction mode, apparatus 110 diffracts input optical beam 20 primarily in a direction as indicated by output rays 30 and 32. By varying the angle THETA between strip position 114' position 114", apparatus 110 can control the phase difference introduced  
30 between rays 30 and 32, and therefore control the intensity of the output signal.

In this example, rotation of strips 114 is accomplished by creating a voltage differential between

inner conductive layer 140 and first portion 115 of reflective conducting layer 117. Because second portion 118 of reflective conducting layer 117 is electrically coupled to inner conductive layer 140, little or no electrostatic force is generated between inner conductive layer 140 and second portion 118 of reflective conducting layer 117. Because, however, first portion 115 of reflective conducting layer 117 is electrically isolated from inner conductive layer 140, a voltage difference between those substantially conducting structures creates an electrostatic force, which operates to pull first portion 115 toward inner conductive layer 140. This, in turn, operates to partially rotate strip 114, causing diffraction of the majority of input beam 20 in one direction as indicated by output rays 30 and 32. The example shown in FIGURE 4 is intended to illustrate one possible embodiment of apparatus 110. Various changes to the configuration and materials described herein could be made without departing from the scope of the invention.

FIGURES 5a-5c are cross-sectional and planar diagrams showing still another example of a mechanism operable to generate and apply a force (F) to cause a partial rotation of strips 14 in a blazed grating apparatus 115. In addition, the embodiment shown in FIGURES 5a-c operates to cause alternate strips 14 to not only partially rotate, but also to move in their entirety toward substrate 12. The result of this alternating strip translation is a diffraction mode of operation wherein all strips 14 are partially rotated, and wherein alternate strips reside in different planes relative to their adjacent strips 14. This configuration can provide additional phase shift between diffracted output rays for a given angle THETA of strip rotation.

In this example, strips 14 are similar in structure and function to strips 14 shown in FIGURES 3a-3c. Strips 14 may comprise any material or combination of materials operable to render an at least substantially conductive and at least partially reflective strip 14. The illustrated embodiment of apparatus 115 includes an inner conductive layer 240. Inner conductive layer 240 in this embodiment, comprises alternating sets of single elongated conductors 48 and pairs 50 of conductor sets 44 and 46, all disposed outwardly from substrate 12. Single elongated conductors 48 and conductors 46 of set of conductors 50 reside approximately inwardly from the edges of strips 14 desired to be rotated toward substrate 12. Conductors 46 also reside inwardly from strips 14, but are electrically separated from conductors 44. The illustrated embodiment provides just one example of a conductor configuration operable to achieve the above-described results. Other configurations could be used without departing from the scope of the invention.

In operation, where it is desired to switch blazed grating apparatus 115 from a reflection mode to a diffraction mode, a voltage differential is created between strips 14 and inner conductive layer 240. In this particular embodiment, a first voltage differential is created between strips 14 and conductors 26, and a second and larger voltage differential is created between strips 14 and conductors 44 and 48. Creating a voltage differential between edges 38 of each strip and the conductors 44 and 48 residing inwardly from those edges causes all strips to rotate. In addition, creating a voltage differential between all or a portion of the remainder of alternated strip cross sections and conductors 46 causes alternate strips to move inwardly

relative to adjacent strips. By creating a larger voltage differential between strips 14 and conductors 44,48 (which pull edges 38 toward substrate 12) than the differential between strips 14 and conductors 46 (which  
5 pull edges opposite edges 38 of the alternate strips toward substrate 12), this arrangement facilitates rotating all strips 14 while pulling alternate strips 14 closer to inner conductive layer 240 than adjacent strips 14.

10 In a reflection mode of operation, blazed grating apparatus 115 receives optical input beam 20, and reflects beam 20 at an angle equal to the angle of incidence of beam 20. Where strips 14 receive beam 20 at a normal incident angle, output beam 24 is reflected at  
15 an angle normal to strips 14. In a diffraction mode of operation, all strips 14 partially rotate toward substrate 12, and alternate strips 14 move inwardly toward substrate 12. The motion of strips 14 results in a phase shift between portions of the output beam, which  
20 may create constructive or destructive interference, depending on the grating angle THETA and amount of relative motion between adjacent strips.

Although this example describes a normal incident input beam, other angles of incidence could be used.

25 FIGURES 6a-6c are cross-sectional and planar diagrams showing still another embodiment of a blazed grating apparatus 120. Blazed grating apparatus 120, in this embodiment, includes a plurality of adjacent strips 214 disposed outwardly from substrate 12. In this  
30 example, each strip 214 has an approximately triangular shaped cross-section. Each strip comprises an outer surface 215 formed at an angle THETA to its inner surface 211. In a particular embodiment, each strip 214 may be



formed from one or more at least partially reflective and at least substantially conductive materials.

Strips 214 may be formed, for example, by repeatedly disposing layers of conductive reflective material, one  
5 on top of the other, and after each new layer is disposed, etching a portion of the new layer to expose a portion of the underlying layer. By repeating those processing steps, a staircase-like structure can be formed comprising numerous layers of reflective  
10 conductive material, wherein the staircase provides an approximate angle THETA measured from the base of the staircase to its top point.

Apparatus 120 includes an inner conductive layer 340 disposed outwardly from substrate 12 and inwardly from  
15 strips 214. In this embodiment, inner conductive layer 340 comprises a plurality of at least substantially conductive strips 340 disposed inwardly from alternate strips 214. As a particular example, conductors of inner conductive layer 340 may comprise strips having a width  
20 approximately equal to the width of strips 214. Other configurations of inner conductive layer 340 may be used without departing from the scope of the invention.

In operation, blazed grating apparatus 120 receives optical beam 20, in this case at a normal incident angle,  
25 and diffracts optical beam 20 so that a majority of the diffracted beam travels in one direction as shown by output rays 30 and 32. When strips 214 reside at location 214', a path difference ( $d_{\text{path1}}$ ) is created between output rays. For a given wavelength and a given  
30 strip periodicity, the path difference between output beam portions is dependent on the angle THETA that outer surface 215 of strips 214 makes with inner surface 211 of strips 214. In one mode of operation, this angle THETA

can be selected, for example, to provide a path difference of one wavelength of optical beam 20 when strips 214 reside at position 214'. In that case, the diffracted output rays constructively interfere to render  
5 a maximum intensity output.

In a diffraction mode, alternate strips 214 are pulled toward substrate 12. This may be accomplished, for example, by creating a differential voltage between alternate strips 214 and conducting strips 340  
10 corresponding to those strips 214. Pulling alternate strips 214 toward substrate 12 creates an increased path difference ( $d_{\text{path2}}$ ) between output rays 30 and 32. This increased path difference results in a further phase difference between output rays 30 and 32.

The variable blazed grating apparatus depicted in FIGURES 1-6 can be useful in a myriad of applications. For example, FIGURES 7a and 7b illustrate variable optical attenuators using blazed grating technology. In particular, FIGURE 7a illustrates a variable attenuator  
15 500 operable to receive optical input beam 20 at a normal incident angle and to introduce any desired amount of attenuation into that signal by controlling the grazing angle THETA between blazed grating 10 at position 14' and position 14".

For a given wavelength of optical beam 20 and for a given periodicity of blazed grating 14, variable attenuator 500 can control the amount of phase shift between output rays 30 and 32 by selectively controlling the angle THETA of blazed grating 10 between position 14'  
20 and 14". Blazed grating 10 residing at position 14" operates to diffract the majority of input optical beam 20 in substantially one direction. Path differences

between diffracted output rays result in interference, affecting the intensity of the output beam.

For example, where diffracted rays 30 and 32 are used as an output and when using an approximately normal  
5 incident input signal, by choosing an angle THETA to result in a path difference ( $d_{\text{path}}$ ) of one wavelength, output rays 30 and 32 constructively interfere with one another, maximizing the intensity of the output beam. At the opposite end of the spectrum, by choosing an angle  
10 THETA to result in a path difference ( $d_{\text{path}}$ ) of one-half wavelength (or an odd multiple thereof), output rays 30 and 32 destructively interfere with each other to completely cancel the output beam. Selectively controlling the position of blazed grating 10 through a  
15 control voltage attenuator 500 to provide any level of output signal between zero and maximum intensity.

The foregoing example has assumed that diffracted rays 30 and 32 are used as an output to attenuator 500. Alternatively, reflected rays, such as ray 24 could serve  
20 as the output to attenuator 500. In that embodiment, the intensity of rays 24 is a maximum when blazed grating 10 operates to reflect substantially all of input beam 20. As the diffraction efficiency of blazed grating 30 increases, the intensity of reflected output rays 24  
25 generally decreases. Blazed grating 10 can, therefore, selectively attenuate output rays 24 relative to input beam 20 by changing its position in response to a control signal thereby changing the diffraction efficiency of diffracted rays 30 and 32, and the intensity of reflected  
30 rays 24.

FIGURE 7b illustrates another variable attenuator 510. Variable attenuator 510 is similar in structure to variable attenuator 500 shown in FIGURE 7a. Variable

attenuator 510, however, operates with an input optical beam 20 having a non-normal incident angle. In particular, in this embodiment variable attenuator 510 operates in the Littrow condition where the angle of incidence of optical beam 20 equals the angle of diffraction of output portions 30 and 32. As previously discussed, this configuration facilitates attaining a given phase difference while requiring only a portion of the blazed grating displacement associated with other configurations.

Variable attenuators implementing blazed grating technology provide a significant advantage of increasing the efficiency and performance of the device, by diffracting a majority of the input beam in a single direction. For example, one embodiment of the invention increases the intensity of the output beam, without requiring additional optical components to collect and recombine diffracted output portions traveling in different directions.

By implementing variable blazed gratings, such as those depicted in FIGURES 1-6, one aspect of the invention facilitates true variability in attenuation by providing a plurality of strips having widths no greater than 40 microns, thus ensuring a wide selection of blaze angles. Unlike other approaches, which are limited to blaze angles of less than two degrees, and therefore limited variation in the amount of attenuation introduced, this aspect of the invention provides an advantage of true variable attenuation. In another aspect of the invention, the apparatus shown in FIGURES 1-6 can operate as a digital switch, for example, by configuring grating 14 to vary its position so that path difference ( $d_{\text{path2}}$ ) results in an odd multiple of one-half

wavelengths of the input optical beam. In that manner, the apparatus can be operated to switch between a first state where output rays constructively interfere to provide a maximum output and a second state where output  
5 rays destructively interfere to reduce or eliminate the output beam.

By appropriate selection of, for example, material type, amounts of materials, grating dimensions, strip tensions, and/or drive voltages, one aspect of the  
10 invention facilitates tuning of switching speeds to maximize switching efficiency. This can be extremely useful in tuning switching speeds to correspond to, for example, common information packet sizes.

For example, the following table shows IP packet  
15 sizes in bytes and the total number of packets percent bytes during the years 1998 and 1999.

	PACKET SIZE (IN BYTES)	TOTAL PACKETS (%)	TOTAL BYTES (%)
20	40	38.9	4.4
	1,500	11.5	48.7
	552	10.1	15.8
	44	6.1	0.8
	576	4.9	7.9

25

This data shows that almost fifty percent of IP packets are between 40 and 44 bytes long. Assuming a data rate of 2.5 Gigabytes per second, switching these packets takes approximately 128 nanoseconds. Thus, for  
30 current packet sizes and data rates, a switching speed of approximately 100 nanoseconds is desirable. Existing switching technologies are either too expensive, or too slow for this application. For example, Lithium Niobate, semiconductor optical amplifiers, or electro-absorption  
35 modulators can switch in less than one nanosecond, a rate

much faster than the optimal 100 nanosecond rate. These devices are prohibitively expensive, particularly when compared to the present invention. In addition, these devices tend to be polarization sensitive. Liquid  
5 crystal devices, thermo-optic devices, and micro-electro-optic switches using a single continuous membrane as a moveable mirror are capable of switching speeds of only up to one microsecond, too slow for optimal operation. Likewise, approaches using wide slightly rotatable slats  
10 to form a variable diffraction grating for high power spectral analyzers are too slow and inflexible in configuration for these applications.

One aspect of the present invention facilitates tuning an optical switch element to provide a desired  
15 switching speed. In a particular embodiment, the switch element can be tuned to provide a switching speed commensurate with a specified packet size or range of packet sizes. For example, the switch element can be tuned to provide switching speeds commensurate with  
20 average packet sizes encountered by the switch element. Various embodiments of the present invention facilitate switching speeds of up to 20 nanoseconds, and can be tuned to provide an optimum switching speed of, for example, approximately 100 to 300 nanoseconds.

25 A separate aspect of the invention, which is not necessarily applicable to only those embodiments shown in FIGURES 1-6 of this disclosure, recognizes that variable blazed gratings, both those described above and some pre-existing designs, can be combined with other optical  
30 components in novel ways to provide useful optical signal processing functions. For example, FIGURE 8 shows a system 531 using a variable blazed grating 10 coupled to a circulator 502.

In some aspects of operation, variable blazed gratings operate to reflect or diffract signals along the signal path of the same or another input signal. To avoid interference between the input signal and the output of the blazed grating, it is desirable to redirect the output signal from the path of the input signal. FIGURE 8 shows an example of a variable blazed grating receiving an optical input signal 520 at a normal incident angle to the grating. In this example, input signal 520 is reflected back along the path of input signal 520 as an output signal 504'. A circulator 502 receives the reflected signal 504' and redirects that signal as output signal 504 away from the path of input signal 520 .

Although this example shows input signal 520 received at a normal incident angle, this aspect of the invention is equally applicable to non-normal incident angles. In addition, although this example shows output signal 504' reflected back along the signal path of its own input signal, the invention can also apply to signals that are reflected or diffracted into the input path of another optical signal. This aspect of the invention finds application anywhere an output signal is directed from a variable blazed grating into the path of an incoming signal.

System 531 advantageously couples a circulator with the variable blazed grating to redirect the output signal 504' without incurring a three decibel loss between redirected output signal 504 and reflected output signal 504', as has been the case in previous approaches using beam splitters to redirect signals. In addition, circulator 502 operates to isolate reflected output signal 504' from adversely affecting input signal 502.

The unique combinations of variable blazed gratings and circulators facilitates system configurations in optical switches, optical add/drop multiplexers, and wavelength division add/drop multiplexers to name a few examples.

5        FIGURES 9a-9b are block diagrams illustrating particular examples of 1x2 optical switches using variable blazed grating devices 10 coupled to optical circulators. Optical switch 600 utilizes an optical input beam 20 having a normal angle of incidence. In  
10 addition to blazed grating device 10, optical switch 600 includes a circulator 602 coupled between input beam 20 and strips 14 of blazed grating 10.

Optical switch 600 can switch input beam 20 to a first output 604 or a second output 606, depending on the  
15 position of blazed grating 10. In a reflection mode of operation, where blazed grating 10 resides at position 14', input optical beam 20 is reflected off of blazed grating 10, and back to circulator 602 where the signal is routed to first output 604. Optical switch 600 can  
20 switch input optical beam 20 to second output 606 by displacing blazed grating 10 to position 14". In this position, a majority of input optical beam 20 will be diffracted in a single direction, in this case the direction indicated as output 606. For a given  
25 wavelength of optical input beam 20 and a given periodicity of blazed grating 10, an angle THETA can be selected to result in a full wavelength phase difference between diffracted portions of input beam 20 to result in a maximum input intensity.

30        FIGURE 9b illustrates an example of another 1x2 optical switch 610. In this case, optical switch 610 operates with a non-normal incident optical signal 20. In particular, optical switch 10 operates in the Littrow



condition where the angle of incidence of optical beam 20 equals the angle of diffraction of the output rays.

In a reflection mode of operation, optical switch 610 can communicate optical input beam 20 toward first  
5 output 614 by having that beam reflected off of blazed grating 10 positioned at location 14'. Optical switch 610 can also switch input optical beam 20 to second output 616 by operating in a diffraction mode where blazed grating 10 is displaced to position 14", and input  
10 beam 20 is diffracted back toward a circulator 612. Circulator 612 directs the output beam toward second output 616. By using non-normal angles of incidence, the angle THETA necessary to achieve the desired phase shift can be reduced as compared to other configurations.

15 FIGURES 10a-10d are block diagrams illustrating various embodiments of 2x2 optical switches constructed from variable blazed gratings and optical circulators. FIGURES 10a and 10b show an embodiment using one normal incident and one non-normal incident input signal.  
20 FIGURES 10c and 10d show an embodiment using two non-normal incident input signals.

In the embodiment shown in FIGURES 10a-10b, 2x2 optical switch 700 receives one optical input beam 20a at a normal incident angle, and receives another optical  
25 input beam 20b at a non-normal incident angle. FIGURE 10a illustrates operation of 2x2 optical switch 700 in a reflection mode. In reflection mode of operation, optical switch 700 receives input optical beams 20a and 20b, and reflects each signal at an angle equal to its  
30 angle of incidence. In particular, blazed grating 10 residing at position 14' receives input optical beam 20a at a normal incident angle and reflects that beam back to a circulator 702, which directs the beam to first output

704. In a similar manner, blazed grating 10 residing at position 14' receives input optical beam 20b at angle PHI and reflects that signal toward a partially reflective surface 711, which is positioned to reflect a signal back toward blazed grating 10 at the same angle PHI and ultimately back to a circulator 708, which directs the beam to second output 706.

FIGURE 10b illustrates the same optical switch 700 operating in a diffraction mode. In diffraction mode, blazed grating 10 is displaced to position 14" at an angle THETA from position 14'. Blazed grating 10 receives optical input beam 20a and diffracts a majority of that beam toward circulator 708, which directs that portion of the beam to output 706. In a similar manner, blazed grating 10 positioned at location 14" receives input optical beam 20b and diffracts a majority of that beam toward circulator 702, which directs the beam toward output 704.

In the embodiment shown in FIGURES 10c-10d, 2x2 optical switch 700 receives both optical input beams 20a and 20b at a non-normal incident angle and operate in a Littrow condition. FIGURE 10c illustrates operation of 2x2 optical switch 700 in a reflection mode. In reflection mode of operation, optical switch 700 receives input optical beams 20a and 20b, and reflects each signal at an angle equal to its angle of incidence. In particular, blazed grating 10 residing at position 14' receives input optical beams 20a and 20b at angle PHI and reflects those signals toward circulator 702 and 708. Circulators 702 and 708 redirect the reflected signals from the path of input signals 20a and 20b to avoid interference between the signals.

FIGURE 10d illustrates the same optical switch 700 operating in a diffraction mode in the Littrow condition. In diffraction mode, blazed grating 10 is displaced to position 14" at an angle THETA from position 14'. Blazed  
5 grating 10 receives optical input beam 20b and diffracts a majority of that beam back toward circulator 708, which directs that portion of the beam to output 706. Blazed grating 10 positioned at location 14" also receives input optical beam 20a and diffracts a majority of that beam  
10 toward circulator 702, which directs the beam toward output 704.

These examples show two particular configurations for using a blazed grating as a 2x2 optical switch. It should be noted that any number of 2x2 optical switches  
15 can be combined to form an array of nxn switches. Moreover, although particular configuration has been described with respect to FIGURES 10a-10d, numerous modifications could be made without departing from the scope of the invention. For example, switches  
20 implementing different geometric configurations, or different numbers of blazed grating elements, circulators, reflective surfaces, or other optical elements are contemplated as being within the scope of the invention.

25 FIGURES 11a-11h show illustrative examples of various embodiments of optical add/drop multiplexers using blazed grating technology coupled with optical circulators. FIGURES 11a-11b show examples of a reflection mode of operation and a diffraction mode of  
30 operation, respectively, of one embodiment of an optical add/drop multiplexer 800 using a blazed grating 10. In this embodiment, reflection mode of add/drop multiplexer 800 corresponds to a pass-through mode of operation. In

this example, blazed grating element 10 receives input optical beam 820a at a non-normal incident angle  $\Phi$ , and is operable to receive added optical beam 820b at a normal incident angle. While blazed grating 10 resides at position 14', input optical beam 820a is reflected from to a mirror 811, which reflects the signal back to blazed grating 10 and on to a circulator 806. Circulator 806 directs input optical beam 820a to output port 806 for pass through operation.

10 This embodiment of add/drop multiplexer 800 facilitates add/drop operation by operating in a diffraction mode. In this example, for operation in add/drop mode, blazed grating 10 is displaced to position 14" at an angle  $\Theta$  to its original position 14".

15 Input optical beam 820a impinges on blazed grating 10 at position 14". The majority of input optical beam 20a is diffracted in a single direction, in this case, toward circulator 802, which directs those diffracted portions of input beam 820a toward drop port 806. Blazed grating

20 10 residing at position 14" receives added optical signal 820b and diffracts the majority of that signal toward circulator 808, which receives the majority portions of the diffracted added signal 820b, and directs those signal portions to output port 806.

25 Add/drop multiplexer 800 provides an advantage of facilitating pass-through operation when no voltage is applied to blazed grating elements 10. In this manner, multiplexer 800 facilitates some level of fault tolerance, in the event of a failure of one or more

30 blazed grating elements 10.

FIGURES 11c-11d show additional examples of a reflection mode of operation and a diffraction mode of operation, respectively, of another embodiment of an

optical add/drop multiplexer 805 using a blazed grating 10. Add/drop multiplexer 805 is similar in structure and function to add/drop multiplexer 800, but uses different ports for receiving input and added signals 820a, 820b.

5        In this embodiment, reflection mode of add/drop multiplexer 805 corresponds to an add/drop mode of operation. In this example, blazed grating element 10 receives input optical beam 820 at a normal incident angle, and receives added optical beam 820b at a non-  
10    normal incident angle  $\Phi$ . With blazed grating 10 residing at position 14', input optical beam 820a is reflected back to a circulator 802, which directs input optical beam 820a to be dropped at port 804. blazed grating 10 receives added optical beam 820b at incident  
15    angle  $\Phi$ , and reflects that beam toward a mirror 811. Mirror 811 reflects the beam back to blazed grating 10 at the same angle  $\Phi$ , causing added signal 820b to be directed toward a circulator 808. Circulator 808 communicates added signal 820b as output 806.

20        This embodiment of add/drop multiplexer 805 facilitates pass-through operation by operating in a diffraction mode. In this example, for operation in pass-through mode, blazed grating 10 is displaced to position 14" at an angle  $\Theta$  to its original position  
25    14". Input optical beam 820a impinges on blazed grating 10 at position 14". The majority of input optical beam 20a is diffracted in a single direction, in this case, toward circulator 808, which directs those diffracted portions of input beam 820a toward output 806.

30        Although FIGURES 11a-11d depict configurations where circulator 802 couples to drop port 804 and circulator 808 couples to output port 806, the invention also contemplates coupling circulator 802 to output port 806

and coupling circulator 808 to drop port 804. In a manner analogous to that described above, input signal 820a and added signal 820b can each be applied along the path of circulator 802 or circulator 808, depending on  
5 the particular device characteristics desired.

FIGURES 11e-11f show still another example of an add/drop multiplexer 810 and its operation in a reflection mode and a diffraction mode, respectively. Optical add/drop multiplexer 810 operates to receive  
10 input optical beams at non-normal angles of incidence PHI. To operate in a pass-through mode, optical add/drop multiplexer 810 operates in reflection mode. In that case, blazed grating 10 remains at position 14', where it receives optical input signal 820a at incident angle PHI,  
15 and reflects that signal at an angle equal to  $180^\circ$  minus PHI toward a circulator 818. Circulator 818 receives optical input beam 820a and directs that beam toward an output 816.

FIGURE 11f shows the same optical add/drop  
20 multiplexer 810 operating in a diffraction mode of operation to effect dropping of the input optical beam 820a in favor of an added optical beam 820b. In particular, when it is desired to drop input optical beam 820a, blazed grating 10 is displaced to position 14".  
25 Blazed grating 10 also receives added optical beam 820b at incident angle PHI to the original position 14'. In this particular example, blazed grating 10 operates in the Littrow condition such that the angle of incidence PHI of added signal 820b equals the angle of diffraction  
30 of output rays 30 and 32. As a result, diffracted portions of added optical beam 820b are communicated back toward circulator 818, which directs those portions of added optical beam 820b to an output 816.

Although FIGURES 11e-11f depict configurations where circulator 812 couples to drop port 814 and circulator 818 couples to output port 816, the invention also contemplates coupling circulator 812 to output port 816 and coupling circulator 818 to drop port 814. In a manner analogous to that described above, input signal 820a and added signal 820b can each be applied along the path of circulator 812 or circulator 818, depending on the particular device characteristics desired.

FIGURES 11g and 11h are block diagrams showing still another embodiment of an optical add/drop multiplexer 850 implementing blazed grating technology. In this example, add/drop multiplexer 850 includes two blazed grating elements 810a and 810b, each communicating with one of circulators 812 and 818. Circulator 812 receives input signal 820a and is coupled to a drop port 814, while circulator 818 receives added signal 820b and is coupled to output port 816.

FIGURE 11g shows add/drop multiplexer 850 in a diffraction mode, which in this case corresponds to a pass through mode of operation. In this mode of operation, circulator 812 passes input signal 820a to blazed grating 810a residing at position 14". A majority of input signal 820a is diffracted toward blazed grating 810b, which also resides at position 14" and operates to diffract a majority of the signal received toward circulator 818. Circulator 818 communicates the diffracted portions of the input signal 820a to output port 816.

FIGURE 11h depicts add/drop multiplexer 850 in a reflection mode, which in this case corresponds to an add/drop mode of operation. In this mode of operation, circulator 812 passes input signal 820a to blazed grating

810a residing at position 14', which reflects input signal 820a back to circulator 812 and on to drop port 814. Circulator 818 passes added signal 820b to blazed grating 810b residing at position 14'. Blazed grating 810b reflects added signal 820b back to circulator 818,  
5 which directs added signal 820 toward output port 816.

Again, the location of inputs for input signal 820a and added signal 820b could be reversed without departing from the scope of the invention. In addition the connections of circulators 812 and 818 to output port 816  
10 and drop port 814 could be reversed without departing from the scope of the invention. The application of input signals and added signals to particular input ports and the designation of particular ports as either drop  
15 ports or output ports merely affects the modes of operation when blazed gratings 10 reside in reflection or diffraction modes.

Another aspect of the invention involves constructing an array of variable blazed grating devices and coupling those devices to a wavelength division demultiplexer to facilitate signal processing one  
20 wavelength or a subset of wavelengths using blazed grating technology. FIGURE 12 is a block diagram showing an array 510 of variable blazed grating devices coupled to a wavelength division demultiplexer 535. In this  
25 example, array 510 is further coupled to a wavelength division multiplexer 534.

In operation, wavelength division demultiplexer 535 receives an optical input signal 520 carrying a plurality of wavelength signals 520a-520n. In this disclosure,  
30 the term "wavelength signal" is used to denote a signal that is part of another signal carrying additional wavelengths beyond those carried in the "wavelength



signal." The term "wavelength signal" does not preclude that signal from itself carrying more than one wavelength of light. In other words, each "wavelength signal" can carry one or a number of wavelengths of light. Input  
5 signal 520 carries a plurality of wavelength signals 520a-520n, which may each carry one or more wavelengths of light.

Wavelength division demultiplexer 535 separates the wavelength signals 520a-520n and communicates at least  
10 some of those signals to array 510 for processing. Wavelength signals 520a-520n communicated to array 510 can undergo various signal processing, such as variable attenuation, optical switching, and/or add/drop multiplexing to name a few examples. Array 510 then  
15 communicates processed signals 520a-520n to an output. In this example, array 510 communicates those signals to wavelength division multiplexer 534 for combination into a multiple-wavelength output signal.

FIGURES 13-15 are block diagrams showing examples of  
20 particular systems implementing arrays of variable blazed grating devices coupled to wavelength division demultiplexers.

FIGURES 13a and 13b are block diagrams showing examples of optical gain equalizers 525 and 545 using  
25 variable blazed grating technology.

FIGURE 13a is a block diagram illustrating an example of one possible embodiment of an optical gain equalizer 525. In this example, optical gain equalizer 525 includes a wavelength division demultiplexer 535  
30 operable to receive an optical signal 520 carrying a plurality of individual wavelength signals 520a-520n. Wavelength division demultiplexer 535 separates optical signal 520 into its a plurality of wavelength signals

520a-520n, each signal carrying one more wavelengths of light.

Optical gain equalizer 525 includes an array 510 of blazed grating devices 10a-10n. In this example, each  
5 blazed grating device 10 operates to produce an output comprising a combination of diffracted output rays 530 and 532. Each blazed grating 10 can provide a selected level of attenuation to the wavelength signal 520a-n it receives, depending on control signals received by the  
10 device. As a particular example, each blazed grating device 10 may comprise a plurality of rotatable strips, and may operate to receive a control voltage that creates a voltage differential between some or all of the rotatable strips and an inner conductive layer. This  
15 voltage differential can be selected to cause a rotation of the strips by an angle THETA, to create a desired phase difference between diffracted output rays 530 and 532, and a corresponding attenuation of the output beam comprising a combination of diffracted rays 530 and 532.  
20 Other blazed grating devices could be used without departing from the scope of the invention.

In this embodiment, optical gain equalizer 525 further includes a wavelength division demultiplexer 534 operable to receive signals processed by blazed grating  
25 devices 10 and to multiplex individual wavelength signals into an optical output signal 536 carrying multiple wavelength signals. In this particular embodiment, reflective surfaces 537a-537n assist in communicating processed signals from blazed grating devices 10 to  
30 wavelength division multiplexer 534. Depending on the desired configuration of blazed grating array 10 relative to wavelength division demultiplexer 532 and wavelength division multiplexer 534, various combinations of

reflective surfaces and other optical communication devices may be implemented without departing from the scope of the invention.

Although the illustrated embodiment describes  
5 processing each wavelength signal 520a-520n, a bypass path could alternatively be provided between demultiplexer 523 and multiplexer 534 to facilitate bypassing blazed grating array 510 for those signals not intended to be processed.

10 FIGURE 13b provides another example of an optical gain equalizer 545 using an array 510 of blazed gratings 10a-10n to facilitate variable attenuation of multiple wavelength signals. Gain equalizer 545 is similar to gain equalizer 525, except gain equalizer 545 uses  
15 reflected rays 524 as output signals, rather than diffracted rays 530, 532 as in gain equalizer 525.

Gain equalizer 545 includes a wavelength division multiplexer/demultiplexer 555, which operates to receive a multiple wavelength signal 520 and to separate optical  
20 signal 520 into multiple wavelength signals 520a-520n, each carrying one or more wavelengths of light. Each wavelength signal 520a-520n is communicated toward one of blazed gratings 10a-10n. Blazed gratings 10a-10n can be controlled through application of control signals to vary  
25 the diffraction efficiency of the blazed grating and, therefore, control the intensity of the reflected output rays 524.

Wavelength division multiplexer/demultiplexer 555 receives reflected rays 524a-524n, and multiplexes those  
30 wavelength signals into a multiple wavelength output signal 524. In this embodiment, a circulator 542 receives output signal 524, and directs that signal away from the path of input signal 520.

Constructing an optical gain equalizer using an array of blazed grating elements provides significant advantages in facilitating large-scale replication of each attenuation stage. For example, numerous blazed  
5 grating stages can be simultaneously formed on a single semiconductor substrate to form an array of blazed grating devices operable to serve any number of individual wavelength signals. One aspect of the invention, therefore, facilitates construction of gain  
10 equalizers capable of processing numerous wavelengths for a small incremental cost over a single stage of attenuators. This aspect of the invention provides significant cost savings in processing signals carrying information on multiple channels or wavelengths.

15 The novel configuration shown in FIGURE 12 can also apply to an optical add/drop multiplexer design. FIGURE 14a is a block diagram illustrating one example embodiment of a wavelength division optical add/drop multiplexer 900. In this example, wavelength division  
20 optical add/drop multiplexer 900 includes a wavelength division demultiplexer 932 operable to receive an optical signal 920 carrying a plurality of individual wavelength signals 920a-920n. Each individual wavelength signal 920a-920n carries one or more wavelengths of information.  
25 Wavelength division demultiplexer 932 communicates individual wavelength signals to an array 910 of optical add/drop multiplexers.

In this example, each add/drop multiplexer of array 910 is similar in structure and function to optical  
30 add/drop multiplexer 805 shown and described with respect to FIGURES 12c and 12d. Other configurations of optical add/drop multiplexers using blazed grating technology could alternatively be implemented without departing from

the scope of the invention. Wavelength division optical add/drop multiplexer also includes a wavelength division multiplexer 934 operable to receive processed signals from array 910 of optical add/drop multiplexers and to  
5 multiplex those individual wavelength signals into an optical output signal 936 carrying a plurality of individual wavelength signals. In operation, wavelength division demultiplexer 932 receives optical signal 920 and separates the individual wavelength  
10 signals 920a-920n. Again, each wavelength signal 920a-920n may include one or more wavelengths of light. In a particular embodiment, wavelength signals not intended to be processed may bypass array 910 of add/drop multiplexers for recombination at multiplexer 934 without  
15 further processing.

Each of the wavelengths to be processed by array 910 is communicated to an optical add/drop multiplexer implementing a blazed grating 10. Optical add/drop multiplexers operate to either pass through the  
20 wavelength signals 920a-920n or to drop those signals at ports 938 in favor of added wavelength signals 940. Wavelength division multiplexer 934 receives processed signals from array 910 and any signals that bypassed array 910 and combines those signals into optical signal  
25 936 carrying a plurality of wavelength signals.

FIGURE 14b is a block diagram illustrating another example of a wave-division optical add/drop multiplexer 950. Wave-division add/drop multiplexer 950 is similar in operation to wave-division add/drop multiplexer 900  
30 shown in FIGURE 13a, except add/drop multiplexer 950 is configured to use common circulators 902 and 908 to be shared among multiple wavelength signals.

Like add/drop multiplexer 900, add drop multiplexer 950 includes a wavelength division demultiplexer 932 operable to receive a multiple wavelength input signal 920 and to separate that signal into a plurality of wavelength signals 920a-920n, each carrying one or more wavelengths of light. Add/drop multiplexer 950 also includes a wave-division multiplexer/demultiplexer 935 operable to receive an added signal 940 containing a plurality of added wavelength signals 940a-940n, and to separate the constituent added wavelength signals 940a-940n.

Add/drop multiplexer 950 further includes an array 910 of blazed gratings 10a-10n. Each blazed grating 10 is operable to receive one of the wavelength signals 920a-920n from demultiplexer 932 and an added signal 940a-940n from wavelength division multiplexer/demultiplexer 935. Depending on the position of each of blazed gratings 10, either the input wavelengths 920 or the added signals 940 received can selectively be communicated toward multiplexer/demultiplexer 935 for communication to a circulator 902 and on to output port 936. Where variable blazed grating 10 operates to reflect or diffract signals back in the direction of any input signal to the system, circulators could be used to redirect the reflected or diffracted signals to enhance system performance. This embodiment provides an advantage of reducing the number of circulators by sharing circulators among a plurality of wavelengths. A similar embodiment could be constructed using multiple arrays of blazed gratings arranged similarly to add/drop multiplexer 850 shown in FIGURES 11g-11h.

As in the case of the blazed grating based gain equalizer, the blazed grating based wavelength division optical add/drop multiplexer provides significant economies over other approaches. For example, by  
5 facilitating fabrication of arrays of blazed grating elements at a fraction of the cost of fabricating a single device, this aspect of the invention provides significant cost savings in processing signals carrying information on multiple channels or wavelengths.

10 The novel configuration shown in FIGURE 12 can also apply to an electro-optic switching system. FIGURE 15 is a block diagram of an exemplary electro-optic switch 1000. Electro-optic router 1000 may include one or more optical amplifiers 1010. In the illustrated embodiment,  
15 an optical amplifier 1010 resides at the ingress end of the switch, which receives optical signals 1012 over a communication link 1020. Electro-optic switch 1000 could also or alternatively include optical amplifiers at the egress end of the switch, or at various other points  
20 within the switch. Optical amplifiers 1010 compensate for losses in the signal and line rates of, for example, OC-48 and OC-192 or higher. In the illustrated embodiment, communication link 1020 comprises a single mode fiber carrying, for example, 100 wavelengths ranging  
25 from 1500 to 1600 nanometers and 2.5 Gb/s or 10 Gb/s per channel.

Optical signal 1012 comprises header information 1014 and signal payload 1016. Electro-optic switch includes a fiber optic tap operable to communicate a  
30 first portion of optical signal 1012 to a delay line 1022 and a second portion of optical signal 1012 to a demultiplexer 1024. In the illustrated embodiment, demultiplexer 1024 may comprise, for example, a

wavelength grating router, operable to split the incoming signal into a plurality of wavelengths and send the plurality of wavelengths to an array of wavelength detectors 1026.

5        Electro-optic switch 1000 also includes an electronic processor 1028 operable to receive optical signals from detectors 1026, to convert the optical signals to electronic signals, and perform various switching, routing, or other processing functions on the  
10 converted electronic signals. Electronic processor 1028 is further operable to convert processed electronic signals into optical signals for transmission to an optical add/drop multiplexer array 1030.

      Electro-optic switch 1000 further includes a  
15 demultiplexer coupled to delay line 1022. In this embodiment, demultiplexer 1032 comprises one or more wavelength grating routers. Both demultiplexer 1032 and electronic processor 1028 communicate with an optical add/drop multiplexer array 1030. In this example,  
20 optical add/drop multiplexer array 1030 comprises an array of blazed grating based add/drop multiplexers, such as those described with respect to FIGURES 12a-12h.

      Each optical add/drop multiplexer of array 1030 receives processed optical header information from  
25 electronic processor 1028 and optical payload information from delay line 1022, and performs various switching functions on those signals. A multiplexer 1036 receives switched optical signals from optical add/drop multiplexer array 1030 and transmits switched optical  
30 signals 1040 to other network elements.

      In operation, electro-optical switch 1000 receives a plurality of optical signals 1012 and depending on, for example, the signal and line rates, may amplify those



signals at optical amplifier 1010. Fiber optic tap 1018 receives optical signals 1012 and sends one copy of the signal including at least header information 1014 to demultiplexer 1024, and sends another copy of the signal  
5 including at least payload information 1016 to delay line 1022.

Delay line 1022 serves as a first-in-first-out (FIFO) buffer. The FIFO buffer length is set so as to provide enough time for electronic processor 1028 to  
10 process the various header information 1014. While payload information 1016 is delayed in FIFO buffer 1022, electronic processor 1028 converts optical header information 1014 into electronic signals, and performs various processing on that header information. After  
15 completing processing of the electronic header information, electronic processor 1028 converts the electronic header information back into one or more optical signals and transmits those signals to optical add/drop multiplexer array 1030.

20 Optical add/drop multiplexer array 1030 receives processed header information and unprocessed payload information 1016, and associates the related payload and header information. Optical add/drop multiplexer array 1030 then switches the processed optical signals at rates  
25 ranging, for example, from approximately 10 to 100 nanoseconds or longer. Multiplexer 1036 receives switched optical signals 1040 from optical add/drop multiplexer array 1030 and transmits the switched optical signals to other network elements.

30 By transmitting the optical payload information transparently to electronic processor 1028, electro-optical switch 1000 advantageously facilitates field coding. As such, header information can be

electronically processed at rates on the order of 2.5 Gigabytes per second, while transparent optical payload information communicates at rates of 10 Gigabytes per second or higher. Electro-optic switch 1000 also  
5 facilitates parallel processing of multiple wavelength channels, increasing the speed and efficiency of the switch. In a particular embodiment, differential logic such as Manchester coding can be used to compensate for switching contrast ratio.

10       FIGURE 16 is a flow chart illustrating one example of a method 400 of optical signal processing using a variable blazed grating based apparatus. Although specific examples of this method use blazed grating 100 as a particular illustrative embodiment, the method could  
15 also be applied to other blazed grating devices, such as those depicted in FIGURES 2-7, as well as systems such as those shown in FIGURES 8-15, and variations of those devices and systems.

      In this example, the method 400 begins at step 410  
20 where blazed grating device 10 receives a first optical signal while residing at a first position 14'. In one particular example, blazed grating 10 could comprise a plurality of at least partially reflective adjacent mirror strips 14. To enhance system speed and  
25 flexibility, in one embodiment, each of the strips could be constructed to have a width of no more than 40 microns. While residing at position 14', mirror strips 14 comprising this example of variable blazed grating 10 may reside approximately parallel to an inner conductive  
30 layer. In another mode of operation, blazed grating 10 can reside at a second position 14", rotated by an angle THETA from the first position 14'.

While blazed grating 10 resides in the first position 14', it operates to reflect the first optical signal in a first direction at step 420. Where first optical signal comprises a normal incident signal, blazed grating 10 operates to reflect signal 20 back in the direction from which it came. Where first optical signal comprises a non-normal incident signal, blazed grating 10 may operate to reflect signal 20 at an angle equal to 180 degrees minus the angle of incidence between signal 20 and strips 14 at position 14'.

At any desired time, blazed grating 10 can be displaced to second position 14" at step 430. As discussed above, while in second position 14", blazed grating 10 resides at an angle THETA from position 14'. While blazed grating 10 resides in second position 14", it diffracts a majority of the first optical signal 20 in a second direction at step 440. Where first optical signal 20 comprises a normal incident signal, blazed grating 10 may diffract a majority of signal 20 at an output angle as shown by output rays 30 and 32 in FIGURE 1b. Where input signal 20 comprises a non-normal incident signal, blazed grating 10 at position 14" may operate to diffract signal portions 30 and 32 as shown, for example, in FIGURE 1c. FIGURE 1c illustrates the Littrow condition, wherein the angle of diffraction equals the angle of incidence of optical signal 20 to blazed grating 10 at position 14". In any case, output rays 30 and 32 exhibit a phase difference that results in a constructive or a destructive interference between those diffracted signal portions.

Where variable blazed grating 10 operates to reflect or diffract signals back in the direction of any input signal to the system, circulators could be used to

redirect the reflected or diffracted signals to enhance system performance.

In one aspect of operation, blazed grating device 10 can operate as a variable attenuator by controlling angle  
5 THETA to result in a particular phase difference between portions of the majority of the diffracted beam. In another aspect of operation, blazed grating device 10 can operate as an optical switch. In that case, it may be desirable to create a phase difference between diffracted  
10 output rays 30 and 32 resulting in a constructive interference between those beam portions to maximize the output signal.

Method 400 applies not only to variable attenuators and 1x2 optical switches, but also to nxn optical  
15 switches and optical add/drop multiplexers. For example, method 400 may continue at step 450 where blazed grating device 10 receives a second optical signal. While blazed grating 10 resides in first position 14', it may reflect a second optical signal in a second direction at step  
20 460. For example, as shown in FIGURES 12a-12b, blazed grating 10 residing at position 14' may reflect first input signal 20a back to circulator 702 and reflect second input signal 20b to second circulator 708. In the case of an optical ad/drop multiplexer, for example,  
25 FIGURES 12a, 12c, and 12e show that input signals and added signals 820a and 820b are reflected by blazed grating 10 at position 14' to circulators 802 and 808 to result in pass-through or add/drop operation, depending on the configuration of the input signals relative to  
30 circulators 802 and 808.

While blazed grating 10 is displaced to position 14", blazed grating 10 diffracts a majority of the second optical signal in the first direction at step 470. In

the case of an optical switch, as shown in FIGURES 12a-12b, when blazed grating 10 is displaced to position 14", it diffracts a majority of first input signal 20a toward second circulator 708, while diffracting a majority of  
5 second input signal 20b toward first circulator 702. In the case of an add/drop multiplexer, as shown, for example, in FIGURES 12b, 12d, and 12f, input and added signals 820a and 820b (which may vary depending on the configuration) can be selectively added or passed through  
10 by diffracting the majority of the signals toward circulators 802 and 808.

Although this method has been described by referencing particular examples of variable attenuators, optical switches, and optical add/drop multiplexers,  
15 various substitutions, alterations and modifications can be made to the configurations described herein without departing from the scope of the invention.

FIGURE 17 is a block diagram of one embodiment of an optical data router 1700. Router 1700 may include one or more separators 1704 each capable of separating an input optical signal 1703 communicated over a communication  
5 link 1702. Separator 1704 may comprise, for example, a wavelength division demultiplexer. As used throughout this document, the phrases "wavelength division multiplexer" and "wavelength division demultiplexer" may include any optical and/or electrical components -  
10 including any hardware, software, and/or firmware - capable of processing wavelength division multiplexed signals and/or dense wavelength division multiplexed signals.

Communication link 1702 can comprise, for example,  
15 standard single mode fiber (SMF), dispersion-shifted fiber (DSF), non-zero dispersion-shifted fiber (NZDSF), dispersion compensating fiber (DCF), or another fiber type or combination of fiber types. In some embodiments, communication link 1702 can operate to couple router 1700  
20 to other optical and/or electro-optical components. For example, link 1702 could couple router 1700 to a cross-connect or another device operable to terminate, switch, route, process, and/or provide access to and/or from communication link 1702 and another communication link or  
25 communication device. As used throughout this document, the term "couple" and or "coupled" refers to any direct or indirect communication between two or more elements, whether or not those elements are physically connected to one another. In some embodiments, communication link  
30 1702 can comprise a point-to-point communication link or a portion of a larger communication network, such as a ring network, a mesh network, a star network, or other network configuration.

Optical signal 1703 may comprise a multiple wavelength optical signal. For example, optical signal 1703 can comprise at least 5 wavelength channels, at least 100 wavelength channels, or at least 250 wavelength channels. In one particular embodiment, optical signal 1703 comprises 250 wavelengths having a 50 GHz spacing within a 100 nm spectral window. In that example, the 100 nm spectral window can be located within the 1400 nm to 1650 nm low-loss window associated with optical fibers. In various embodiments, optical signal 1703 can implement one or more data formats, such as, polarization shift keying (PLSK), pulse position modulation (PPM), Multi-Protocol Label Swapping (MPLS), Generalized Multi-Protocol Label Swapping (GMPLS), non-return to zero (NRZ), return to zero (RZ), differential phase shift key (DPSK), or a combination of these or other format types.

In various embodiments, separator 1704 operates to separate optical signal 1703 into individual wavelength channels 1705 and to couple each wavelength channel 1705 to an input interface 1706. In an alternative embodiment, separator 1704 can separate optical signal 1703 into separate multiple-wavelength channels and couple those multiple-wavelength channels to input interface 1706. Wavelength channels 1705 can comprise, for example, Internet Protocol (IP) packets, voice data, video data, or any other data type and/or data format. In this particular embodiment, each wavelength channel 1705 implements a frame format that comprises one or more framing bits, a first packet label that precedes a packet data, and a second packet label that follows the packet data. Surrounding a packet data with packet labels advantageously allows for relatively simple error checking at a destination associated with each wavelength

channel 1705, however this format is not required. In this example, each wavelength channel 1705 implements a Generalized Multi-Protocol Label Swapping (GMPLS) routing protocol within the first and second packet labels.

5 Although this example implements a GMPLS routing protocol, other routing protocols or data formats may be used without departing from the scope of the present disclosure.

In this example, input interface 1706 operates to

10 receive and process each wavelength channel 1705 associated with optical signal 1703. Input interface 1706 can comprise any optical and/or electrical components - including any hardware, software, and/or firmware - capable of processing, converting,

15 replicating, updating, and/or swapping one or more packet labels associated with each wavelength channel 1705. In various embodiments, input interface 1706 can determine a desired routing for a packet data associated with each wavelength channel 1705 and can update a first and/or

20 second packet label using an all-optical label swapping technique. The phrase "all-optical" refers to the performance of a desired functionality substantially free from optical-to-electrical or electrical-to-optical conversions. The "all-optical" functionality does not

25 prohibit optical-to-electrical or electrical-to-optical conversions for use by control circuitry that contributes to the overall function of the device. For example, input interface 1706 may include a controller that receives an electrical representation of a packet label

30 and generates a control signal that functions to modulate a swapping sequence on an optical signal.

Router 1700 further includes a switching element 1708 capable of processing one or more packet data



associated with wavelength channels 1705 received from input interface 1706 and directing those packet data to a desired destination. Switching element 1708 can comprise any optical and/or electrical components - including any hardware, software, and/or firmware - capable of switching, routing, error checking, and/or managing the one or more packet data or packet labels associated with each wavelength channel 1705. In this particular embodiment, switching element 1708 can comprise a ring configuration having one or more core router nodes and at least one management node. Although this example implements a ring configuration, switching element 1708 could implement a mesh configuration, a star configuration, or any other configuration without departing from the scope of the present disclosure. In various embodiments, switching element 1708 can operate to process wavelength channels 1705 at processing speeds of, for example, at least 10 Gb/s, at least 40 Gb/s, at least 100 Gb/s, or at least 160 Gb/s.

In various embodiments, switching element 1708 can route and/or receive one or more packet data associated with wavelength channels 1705 to and/or from an edge router 1718. In one non-limiting example, edge router 1718 may comprise an electro-optic router capable of providing an optical-to-electrical interface. In other embodiments, switching element 1708 can route one or more packet data associated with wavelength channels 1705 to an output interface 1710. Output interface 1710 can comprise any optical and/or electrical components - including any hardware, software, and/or firmware - capable of preparing one or more packet data associated with wavelength channels 1705 for communication from router 1700. In this example, output interface 1710

operates to communicate the one or more packet data from router 1700 to a desired destination through an appropriate wavelength channel 1713.

Router 1700 may also include one or more combiners  
5 1714 each capable of combining output wavelength channels 1713 into one or more output optical signals 1715 for communication over a communication links 1716. Combiner 1714 may comprise, for example, a wavelength division multiplexer. The structure and function of communication  
10 link 1716 can be substantially similar to the structure and function of communication link 1702. In this example, communication links 1716 operate to couple router 1700 to other optical and/or electro-optical components.

15 In this example, router 1700 also includes a controller 1712 capable of at least partially contributing to controlling one or more functionalities associated with router 1700. That is, controller 1712 is not required to be capable of performing the desired  
20 functionality alone, but may contribute to the performance of the function as part of a larger routine. Controller 1712 can comprise any communication and/or computational device or devices, including any hardware, software, firmware, or combination thereof.

25 In operation, the packet data associated with wavelength channels 1705 are transparent to the processing functions of router 1700. That is, in operation router 1700 does not examine the content of the packet data associated with each wavelength channel 1705.  
30 In some cases, router 1700 does examine the contents of one or more packet labels and/or other elements of a frame format associated with wavelength channels 1705. In most cases, router 1700 operates to maintain the

packet data associated with wavelength channels 1705 in the optical domain. That is, the packet data associated with each wavelength channel 1705 are not subjected to an optical-to-electrical conversion by router 1700. In some cases, one or more of the packet labels and/or other elements of a frame format associated with wavelength channels 1705 can be subjected to one or more optical-to-electrical and/or electrical-to-optical conversions. In various embodiments, router 1700 may be capable of an aggregate capacity of, for example, at least 5 Tb/s, at least 25 Tb/s, at least 50 Tb/s, or at least 100 Tb/s.

In this particular embodiment, input interface 1706, switching element 1708, and/or output interface 1710 can implement one or more semiconductor optical amplifier (SOA) components. Implementing one or more SOA components within input interface 1706, switching element 1708, and/or output interface 1710 can, in some cases, allow at least a portion of these devices to be amenable to semiconductor chip level integration. In some embodiments, input interface 1706, switching element 1708, and/or output interface 1710 can implement one or more semiconductor optical amplifier Mach Zehnder Interferometer (SOA-MZI) based all-optical logic gates capable of 2R regeneration, 3R regeneration, and/or wavelength conversion. The phrase "2R regeneration" as used herein refers to the re-amplification and re-shaping of an optical signal wavelength. As used throughout this document, the phrase "3R regeneration" refers to the re-amplification, re-shaping, and re-timing of an optical signal wavelength.

In some embodiments, router 1700 can operate to minimize and/or avoid contention between packet data associated with optical signals 1703 and 1715 and/or

wavelength channels 1705 and 1713 within switching element 1708 and/or communication links 1702 and 1716. The term "contention" as used herein refers to a process by which a packet data competes with other packet data  
5 for communication over a specific wavelength. In some cases, contention can be minimized by, for example, implementing a ring network architecture or performing wavelength conversion. Minimizing and/or avoiding contention can result in a reduction in the congestion  
10 associated with an optical signal wavelength.

FIGURE 18 is a block diagram of one embodiment of an input interface 1800. In this example, input interface 1800 includes at least a first optical tap 1802a and a second optical tap 1802n. Although this example  
15 implements two optical taps 1802a and 1802n, any number of optical taps can be used without departing from the scope of the present disclosure. Optical taps 1802 can comprise any optical component or components capable of de-coupling or removing at least a fraction of an optical  
20 signal 1820 from an optical fiber 1803. As a non-limiting example, optical taps 1802 can comprise a wavelength division demultiplexer or a power splitter. In various embodiments, the structure and function of optical signal 1820 can be substantially similar to  
25 wavelength channel 1705 of FIGURE 17. In this particular embodiment, optical signal 1820 implements a frame format that comprises one or more framing bits, a first packet label that precedes packet data, and a second packet label that follows the packet data.

30 Input interface 1800 also includes a receiver 1804 capable of receiving at least a portion of optical signal 1820 from optical tap 1802a. Receiver 1804 can comprise any optical and/or electrical component - including any

hardware, software, and/or firmware - capable of identifying and capturing a packet label associated with optical signal 1820. In this example, receiver 1804 converts the received portion of optical signal 1820 to  
5 an electronic signal 1805 representing the packet label and communicates electronic signal 1805 to a controller 1808. Controller 1808 can comprise any device capable of determining one or more routing properties and/or routing rules associated with electronic signal 1805. In this  
10 particular embodiment, controller 1808 accesses to a look-up table and determines where to route optical signal 1820 based at least in part on data contained within the look-up table.

Input interface 1800 further includes a self-sync  
15 module 1806 capable of receiving at least a portion of optical signal 1820 from optical tap 1802n. Self-sync module 1806 can comprise any optical and/or electrical component - including any hardware, software, and/or firmware - capable of extracting, processing, and/or  
20 synchronizing a first bit associated with a packet label optical signal 1820. In various embodiments, self-sync module 1806 can comprise one or more semiconductor optical amplifiers (SOA) capable of functioning as a fast-saturated slow recovery gain medium and/or an  
25 intensity discriminator. Implementing SOA components within self-sync module 1806 advantageously enables at least portions of self-sync module 1806 to be amenable to semiconductor chip level integration.

In this particular embodiment, self-sync module 1806  
30 synchronizes the first bit associated with a packet label of optical signal 1820 and communicates a synchronized signal 1807 to an all-optical label swapping module 1810. In one non-limiting example, synchronized signal 1807

comprises the first bit associated with the packet label of optical signal 1820. Label swapping module 1810 may comprise any optical and/or electrical component - including any hardware, software, and/or firmware -  
5 capable of processing, replicating, converting, updating, and/or swapping one or more packet labels associated with optical signal 1820. In various embodiments, swapping module 1810 can comprise one or more semiconductor optical amplifiers (SOA) capable of functioning as an  
10 optical logic gate.

Label swapping module 1810, in this example, operates to replicate synchronized signal 1807 once for each bit in the packet label associated with optical signal 1820. For example, if the packet label comprises  
15 eight bits, then label swapping module 1810 replicates synchronized signal 1807 eight times. In various embodiments, label swapping module 1810 may comprise, for example, a multi-mode interference coupler or any other device capable of replicating synchronized signal 1807.  
20 In addition, label swapping module 1810 operates to encode these replicated bits with a swapping sequence based at least in part on a control signal 1809 received from controller 1808. Label swapping module 1810 may comprise, for example, a modulator or any other device  
25 capable of encoding the swapping sequence onto the replicated bits.

In this example, label swapping module 1810 receives optical signal 1820 from a delay line 1812. Delay line 1812 may comprise, for example, a sufficient length of  
30 optical fiber capable of compensating for any processing latency associated with receiver 1804, module 1806, and/or controller 1808. In some cases, delay line 1812

may operate as a first-in-first-out (FIFO) buffer having an appropriate length.

In this particular embodiment, label swapping module 1810 implements an all-optical XOR gate capable of  
5 updating and/or swapping the packet labels associated with optical signal 1820 based at least in part on control signal 1809. In some cases, the all-optical XOR gate can operate at processing speeds of up to 160 Gb/s. In various embodiments, the all-optical XOR gate may  
10 comprise, for example, a semiconductor optical amplifier Mach Zehnder Interferometer (SOA-MZI) capable of 2R regeneration, 3R regeneration, and/or wavelength conversion. Implementing an all-optical XOR gate in label swapping module 1810 can improve, in some cases,  
15 the processing efficiency of label swapping module 1810. That is, if the packet label and packet data have the same bit rate, then frame generation and synchronization can be simplified. In addition, implementing an all-optical XOR gate can minimize and/or eliminate the need  
20 for a time guard band between the packet label and the packet data.

In this particular embodiment, label swapping module 1810 is configured to optionally update and/or swap the packet labels associated with optical signal 1820,  
25 improve the optical signal-to-noise ratio (OSNR) through re-amplification of optical signal 1820, and communicate optical signal 1820 to a switching element coupled to input interface 1800. In some embodiments, label swapping module 1810 can convert the wavelength  
30 associated with optical signal 1820 to another wavelength. In some cases, converting the wavelength associated with optical signal 1820 can advantageously

reduce and/or minimize contention within a switching module coupled to input interface 1800.

FIGURE 19 is a block diagram of one embodiment of a self-sync module 1900. In various embodiments, the structure and function of self-sync module 1900 can be substantially similar to the structure and function of self-sync module 1806 of FIGURE 18. In this example, module 1900 includes a gain medium 1902 capable of receiving an optical signal 1906. In this particular embodiment, gain medium 1904 comprises a fast-saturated slow recovery gain medium having a time decaying transmission function that operates to create higher intensity within a first bit 1906a associated with optical signal 1906. In some embodiments, gain medium 1902 can comprise a semiconductor optical amplifier (SOA) having a fast-saturated slow recovery transmission function. In various embodiments, the structure and function of optical signal 1906 can be substantially similar to wavelength channel 1705 of FIGURE 17. In this particular example, optical signal 1906 comprises a packet label having five bits, where first bit 1906a comprises a value of '1'. In some cases, optical signal 1906 may comprise a time guard band capable of allowing gain medium 1902 to recover before processing a subsequently received optical signal. In those cases, the time guard band may comprise, for example, approximately 100 picoseconds (ps) or more, 400 ps or more, or any other appropriate period.

Module 1900 also includes an intensity discriminator module 1904 capable of selecting one or more bits having an intensity above a desired threshold. Intensity discriminator may comprise, for example, a nonlinear optical loop mirror having a nonlinear phase shift, a



dispersion-shifted fiber having a nonlinear spectral shift, or any other device capable of selecting one or more bits having an intensity above a desired threshold. In one particular embodiment, intensity discriminator  
5 module 1904 comprises an SOA-MZI having a sine-squared transfer function at relatively low intensities. Implementing SOA components within gain medium 1902 and intensity discriminator module 1904 advantageously enables at least portions of module 1900 to be amenable  
10 to semiconductor chip level integration. In various embodiments, implementing SOA components within gain medium 1902 and intensity discriminator module 1904 can result in the synchronization of the processed portion of optical signal 1906 being desensitized to inter-packet  
15 jitter before use by an all-optical logic gate.

In operation, gain medium 1902 receives optical signal 1906 comprising five bits from an optical tap and creates a higher and/or desired intensity within first bit 1906a. Intensity discriminator receives the five  
20 bits associated with optical signal 1906 and selects first bit 1906a. In this example, first bit 1906a comprises an intensity that is above a desired threshold, while the remaining four bits have intensities below the desired threshold. In one example, module 1900 can  
25 communicate first bit 1906a to an all-optical label swapping module.

FIGURE 20 is a block diagram of one embodiment of an all-optical label swapping module 2000. In various embodiments, the structure and function of label swapping  
30 module 2000 can be substantially similar to the structure and function of label swapping module 1810 of FIGURE 18. In this example, label swapping module 2000 includes a synchronized signal replication module 2004 capable of

replicating synchronized signal 2002 once for each bit in the packet label associated with optical signal 2016. For example, if the packet label comprises ten bits, then replication module 2004 replicates synchronized signal 2002 ten times. Replication module 2004 may comprise any optical and/or electrical components - including any hardware, software, and/or firmware - capable of replicating synchronized signal 2002. In this particular embodiment, replication module 2004 comprises a multi-mode interference coupler. In various embodiments, the structure and function of synchronized signal 2002 can be substantially similar to the structure and function of optical signal 1906 of FIGURE 19.

In this example, replication module 2004 communicates a replicated signal 2006 comprising a plurality of bits to an encoding module 2008. In this example, the number of bits of the replicated signal equal the number of bits in the corresponding packet label. Encoding module 2008 may comprise, for example, a modulator or any other device capable of encoding a swapping sequence onto the replicated bits. In this particular embodiment, encoding module 2008 operates to encode replicated signal 2006 based at least in part on a control signal 2010 comprising a swapping sequence.

Label swapping module 2000 also includes an all-optical XOR gate 2014. In this example, gate 2014 operates to update and/or swap the packet labels associated with optical signal 2016 based at least in part on an encoded signal 2012. All-optical XOR gate 2014 may comprise, for example, a semiconductor optical amplifier Mach Zehnder Interferometer (SOA-MZI) capable of 2R regeneration, 3R regeneration, and/or wavelength conversion. Implementing SOA components within XOR gate

2014 advantageously enables at least portions of module 2000 to be amenable to semiconductor chip level integration. In addition, implementing all-optical XOR gate 2014 in label swapping module 2000 can improve, in  
5 some cases, the processing efficiency of label swapping module 2000. That is, if the packet label and packet data have the same bit rate, then frame generation and synchronization can be simplified. Moreover, implementing XOR gate 2014 can minimize and/or eliminate  
10 the need for a time guard band between the packet label and the packet data of optical signal 2016. Furthermore, implementing XOR gate 2014 allows label swapping to be transparent to the frame format containing the packet data and the bit rate.

15 In this particular embodiment, label swapping module 2000 is configured to optionally update and/or swap the packet labels associated with optical signal 2016 and to improve the optical signal-to-noise ratio (OSNR) through re-amplification and re-shaping of optical signal 2016.  
20 In some embodiments, label swapping module 2000 can also operate to convert the wavelength associated with optical signal 2016 to another wavelength.

In operation of this example, XOR gate 2014 updates and/or swaps each bit of the packet label associated with  
25 optical signal 2016 when the corresponding bit associated with encoded signal 2012 comprises a value of "1." by contrast, XOR gate 2014 does not update and/or swap the bits of the packet label associated with optical signal 2016 when the corresponding bit associated with encoded  
30 signal 2012 comprises a "0." For example, if a packet label associated with optical signal 2016 comprises 110010 and the desired output packet label is 100111. Then, the swapping sequence encoded on signal 2012 would

comprise 010101. In an alternative embodiment, XOR gate 2014 can operate to swap and/or update each bit of the packet label associated with optical signal 2016 when the corresponding bit associated with encoded signal 2012  
5 comprises a value of "0" and not to swap values then the corresponding bit of encoded signal 2012 has a value of "1."

FIGURE 21 is a block diagram of one embodiment of a replication module 2100 implementing a multi-mode  
10 interference coupler 2104. In various embodiments, the structure and function of replication module 2100 can be substantially similar to the structure and function of replication module 2004 of FIGURE 20. In this example, replication module 2100 operates to replicate  
15 synchronized signal 2102 once for each bit in the packet label associated with an optical signal. For example, if the packet label comprises four bits, then replication module 2100 replicates synchronized signal 2102 four times resulting, in this example, in four replicated  
20 signals, each comprising one bit. In various embodiments, the structure and function of synchronized signal 2102 can be substantially similar to the structure and function of synchronized signal 2002 of FIGURE 20.

In this example, replication module 2100 includes at  
25 least a first multi-mode interference (MMI) coupler 2104a and a second MMI coupler 2104b. Although this example implements two MMI couplers, any number of MMI couplers may be used without departing from the scope of the present disclosure. In this example, first MMI coupler  
30 2104a is capable of replicating the bit associated with synchronized signal 2102 into a desired number of bits and communicates those bits to second MMI coupler 2104b through waveguides 2105a-2105n. Second MMI coupler 2104b

operate to combine the replicated bits into a replicated optical signal 2106 for further processing. In this example, replicated optical signal 2106 comprises four bits, with appropriate delay between bits.

5 Waveguides 2105 operate to provide adequate delay between the replicated bits. In some cases, waveguides 2105 can operate to provide a delay between bits of at least 25 ps for packet labels communicated at approximately 40 Gb/s. That is, where a packet label is  
10 communicated at approximately 40 Gb/s, waveguide 2105b operates to cause a bit traversing waveguide 2105b to take approximately 25 ps longer than a bit traversing waveguide 2105a. In other cases, waveguides 2105 can operate to provide a delay between bits of at least 6.25  
15 ps for packet labels communicated at approximately 160 Gb/s. In various embodiment, waveguides 2105a-2105n can be formed on a common photonic integrated circuit using silica-on-silicon optical bench technology and standard semiconductor processing techniques. In other  
20 embodiments, waveguides 2105a-2105n can be formed on a plurality of photonic integrated circuits using silica-on-silicon optical bench technology and standard semiconductor processing techniques.

FIGURES 22A-22D are block diagrams of example  
25 embodiments of all-optical logic gates for bit level processing. In these examples, the all-optical logic gates implement an SOA-MZI capable of 2R regeneration, 3R regeneration, and/or wavelength conversion. In other examples, the all-optical logic gates implement a  
30 Michelson interferometer, a Sagnac interferometer, or another interferometer. Implementing SOA components within the all-optical logic gates advantageously enables the logic gates to be amenable to semiconductor chip

level integration. In one particular embodiment, a plurality of logic gates can be formed on a single semiconductor chip using standard semiconductor processing techniques.

5       FIGURE 22A is a block diagram of one embodiment of an all-optical XOR gate 2200. In this example, XOR gate 2200 includes at least a first SOA 2202a and a second SOA 2202b. As one non-limiting example, SOA 2202a and SOA 2202b can each comprise an InGaAsP semiconductor optical  
10   amplifier. In this particular embodiment, SOA 2202a receives a first data signal 2206a and SOA 2202b receives a second data signal 2206b. In this particular example, SOA 2202a and SOA 2202b are arranged symmetrically to form an integrated semiconductor optical amplifier Mach  
15   Zehnder Interferometer (SOA-MZI). In various embodiments, XOR gate 2200 can be formed on a single semiconductor substrate using standard semiconductor processing techniques. In those cases, the semiconductor substrate may comprise, for example, silicon, poly-  
20   silicon, indium phosphide, gallium arsenide, or any other suitable substrate material or combination of materials.

First data signal 2206a and second data signal 2206b can each comprise, for example, an optical signal having a swapping sequence, an optical signal having at least  
25   packet data, an optical signal having at least a packet label, or a combination of these or other data types. In various embodiments, the structure and function of first data signal 2206a and second data signal 2206b can be substantially similar to the structure and function of  
30   encoded signal 2012 and/or optical signal 2016 of FIGURE 20. In some cases, control pulses associated with the bits of data signals 2206a and/or 2206b can comprise a pulse width that is longer than a length associated with

SOA 2202a and/or 2202b. For example, if SOA 2202a comprises a length of approximately 500 microns, the pulse width associated with data signal 2202a could comprise approximately five (5) ps.

5 XOR gate 2200 also includes a light source 2204 capable of generating a CLK signal 2208 at one or more desired wavelengths. In one particular embodiment, light source 2204 and SOA's 2202a and 2202b can be formed on a single semiconductor chip using standard semiconductor  
10 processing techniques. Light source 2204 can comprise, for example, a continuous wave light source, a synchronized light source, a tunable light source, a semiconductor laser, a laser diode, a cladding pump fiber laser, or any combination of these or any other light  
15 source. The phrase "synchronized light source" as used herein refers to a light source that provides a pulsed and/or modulated output signal that is capable of being synchronized to an incoming data stream using a phase locked loop.

20 In various embodiments, CLK signal 2208 can comprise the same wavelength as data signals 2206 or can comprise a different wavelength than data signals 2206. Implementing CLK signal 2208 at the same wavelength as data signals 2206 can allow system designers to implement  
25 multiple levels of logic without the need for wavelength conversion at each logic level. In some cases, implementing a different CLK signal 2208 wavelength can result in a wavelength shift and possibly minimize or avoid contention within a network, optical device, and/or  
30 switching element coupled to XOR gate 2200.

In this particular embodiment, CLK signal 2208 traverses SOA 2202a and SOA 2202b in a direction that is counter to a direction that data signals 2206 traverse

SOA 2202a and 2202b. Implementing a counter-propagating CLK signal 2208 allows CLK signal 2208 and data signals 2206 to use the same optical signal wavelength. In addition, counter-propagating CLK signal 2208 and data signals 2206 allows data signals 2206 to be de-coupled from XOR gate 2200 using an isolator or circulator. In some embodiments, counter-propagating CLK signal 2208 and data signals 2206 can achieve polarization independence. In those cases, SOA 2202a and SOA 2202b may comprise a polarization independent SOA. In other embodiments, SOA 2202a and 2202b can comprise a polarization dependent loss of, for example, no more than three (3) dB, two (2) dB, or one-half (0.5) dB. Thus, implementing a counter-propagating CLK signal 2208 can advantageously enable XOR gate 2200 to provide three-terminal, cascadable, and polarization independent operation.

In operation, when XOR gate 2200 receives either data signal 2206a or 2206b a change in phase associated with one arm of XOR gate 2200 results in an imbalance in the interferometer and an output from XOR gate 2200. For example, when SOA 2202a receives data signal 2206a a change phase occurs in the arm of XOR gate 2200 comprising SOA 2202a, which results in an output from XOR gate 2200. The change in phase associated with one arm of XOR gate 2200 results from cross-phase modulation. On the other hand, when XOR gate 2200 receives both data signals 2206a and 2206b the cross-phase modulation phase shift is equal in the two arms of XOR gate 2200 resulting in a null output. In this example, XOR gate 2200 is biased such that when both of data signals 2206a and 2206b are not received by XOR gate 2200 then a null output is achieved.



In this particular embodiment, XOR gate 2200 is capable of functioning as a regenerative logic gate. XOR gate 2200 is capable of achieving a regenerative function by replacing the incoming photons associated with data  
5 signals 2206 with new photons from light source 2204. Implementing a regenerative function within XOR gate 2200 can operate to minimize the accumulation of distortions when several logic gate are cascaded. In addition, XOR gate 2200 is capable of functioning as a 2R regenerator  
10 data signals 2206, because the transfer function of XOR gate 2200 comprises a sine-squared, nonlinear operation. That is, the transfer function of XOR gate 2200 can operate to suppress noise at low intensity since the sine-squared function is approximately quadratic at low  
15 intensities. Power levels of data signals 2206 and/or CLK signal 2208 can be biased to be in the approximately linear regime of the transfer function for high throughput.

In this particular embodiment, XOR gate 2200 is  
20 capable of ultrafast operation. XOR gate 2200 is capable of ultrafast operation by implementing a symmetric structure and biasing at the gain transparency of SOA 2202a and SOA 2202b. Implementing a symmetric structure in XOR gate 2200 advantageously minimizes and/or cancels  
25 the slow non-linearity effects, which leads to switching based only on the fast non-linearity effects of the SOAs. In addition, biasing SOA 2202a and SOA 2202b at transparency advantageously minimizes and/or reduces bit-pattern dependence and inter-symbol interference (ISI)  
30 because few or no net carriers remain after the pulses pass through. In various embodiments, carrier heating and two-photon absorption can aid in the fast non-linearity operation of XOR gate 2200. In some cases, XOR

gate 2200 can perform switching operation at speeds of, for example, at least 10 Gb/s, at least 40 Gb/s, at least 100 Gb/s, or at least 160 Gb/s.

In this particular embodiment, XOR gate 2200 is  
5 capable of 2R regeneration that is substantially transparent to a bit rate and data format associated with data signals 2206. In an alternative embodiment, XOR gate 2200 is capable of 3R regeneration. In that case, light source 2204 comprises a synchronized light source  
10 capable of generating a pulsed and/or modulated CLK signal 2208 that is synchronized to data signals 2206 by a phase locked loop. In that case, the synchronized light source operates to provide a re-timing function.

FIGURE 22B is a block diagram of one embodiment of  
15 an all-optical inverter 2225. In various embodiments, the structure and function of inverter 2225 can be substantially similar to the structure and function of XOR gate 2200 of FIGURE 22A. In this example, inverter 2226 includes at least a first SOA 2226a and a second SOA  
20 2226b. The example illustrated in FIGURE 22B differs from the example illustrated in FIGURE 22A in that the CLK signal is inputted into SOA 2226b. Inputting the CLK signal into SOA 2226b and the 'A' signal into SOA 2226a causes the logic gate to function as an inverter.

FIGURE 22C is a block diagram of one embodiment of  
25 an all-optical AND gate 2250. In various embodiments, the structure and function of AND gate 2250 can be substantially similar to the structure and function of XOR gate 2200 of FIGURE 22A. In this example, gate 2250  
30 includes at least a first SOA 2252a and a second SOA 2252b. The example illustrated in FIGURE 22C differs from the example illustrated in FIGURE 22A in that the 'A' signal traverses gate 2250 in a direction counter to

the 'B' signal. Counter propagating the 'A' signal and the 'B' signal as illustrated causes the logic gate to function as an AND gate.

FIGURE 22D is a block diagram of one embodiment of an all-optical regenerator 2275. In various embodiments, the structure and function of regenerator 2275 can be substantially similar to the structure and function of XOR gate 2200 of FIGURE 22A. In this example, gate 2200 includes at least a first SOA 2276a and a second SOA 2276b. The example illustrated in FIGURE 22D differs from the example illustrated in FIGURE 22A in that the 'B' signal does not propagate within regenerator 2275. Counter propagating only the 'A' signal and the CLK signal as illustrated causes the logic gate to function as a buffer switch or regenerator.

FIGURE 23 is a block diagram of one example of an optical switching element 2300. In various embodiments, the structure and function of optical switching element 2300 can be substantially similar to the structure and function of switching element 1708 of FIGURE 17. In some embodiments, switching element may be capable of an aggregate capacity of, for example, 100Tb/s or more. In this example, switching element 2300 includes a backplane 2302 capable of communicating optical signal 2312 within switching element 2300.

Backplane 2302 can comprise one or more optical fibers, such as, for example, standard single mode fiber (SMF), dispersion-shifted fiber (DSF), non-zero dispersion-shifted fiber (NZDSF), dispersion compensating fiber (DCF), or another fiber type or combination of fiber types.

In this particular embodiment, optical signal 2312 implements a frame format that comprises one or more

framing bits, a first packet label that precedes packet data, and a second packet label that follows the packet data. In various embodiments, the structure and function of optical signal 2312 can be substantially similar to  
5 optical signal 1703, optical signal 1715, wavelength channel 1705, or wavelength channel 1713 of FIGURE 17. In other embodiments, optical signal 2312 can implement a time guard band that separates optical signal 2312 from a later received optical signal.

10 Switching element 2300 also includes core router nodes 2304a-2304d capable of processing packets of data associated with optical signal 2312 and communicating those packets to a desired destination. Although this example includes four (4) core router nodes, any number  
15 of core router nodes may be used without departing from the scope of the present disclosure. Core router nodes 2304a-2304d may comprise any optical and/or electrical component - including any hardware, software, and/or firmware - capable of processing, replicating, storing,  
20 converting, routing, and/or error checking one or more packet data associated with optical signal 2312.

In some embodiments, core router nodes 2304a-2304d may be capable of routing and/or receiving one or more packets to and/or from other core router nodes 2308. In  
25 other embodiments, core router nodes 2304a-2304d may be capable of routing and/or receiving one or more packets to and/or from one or more edge routers 2310. In various embodiments, core router nodes 2304a-2304d may be capable of processing optical signal 2312 at a processing speed  
30 of, for example, at least 10 Gb/s, at least 40 Gb/s, at least 100 Gb/s, or at least 160 Gb/s.

Switching element 2300 further includes management node 2306 capable of initializing switching element 2300

at start-up and of performing clean-up of unwanted packets. Management node 2306 may comprise any optical and/or electrical component - including any hardware, software, and/or firmware - capable of processing,  
5 replicating, storing, converting, routing, and/or managing one or more packets associated with optical signal 2312.

In some embodiments, management node 2306 may be capable of dropping and/or removing a packet comprising  
10 an illegal or inoperative packet label. In other embodiments, management node 2306 can operate to minimize the recirculation of optical signal 2312 through backplane 2302 by removing and/or dropping a packet that has circulated backplane 2302 more than one time. In  
15 various embodiments, management node 2306 may be capable of processing optical signal 2312 at a processing speed of, for example, at least 10 Gb/s, at least 40 Gb/s, at least 100 Gb/s, or at least 160 Gb/s.

In various embodiments, core router nodes 2304a-  
20 2304d and/or management node 2306 can comprise one or more semiconductor optical amplifiers (SOA). Implementing SOA components within core router nodes 2304a-2304d advantageously enables at least portions of core router nodes 2304a-2304d to be amenable to  
25 semiconductor chip level integration. In some cases, the implementation of one or more SOA's can allow packets associated with optical signal 2312 to be substantially transparent to the processing functions of core router nodes 2304a-2304d and/or management node 2306. In  
30 addition, core router nodes 2304a-2304d and/or management node 2306 can operate to maintain the packets associated with optical signal 2312 in the optical plane. In other cases, core router nodes 2304a-2304d and/or management

node 2306 can examine the contents of one or more packet labels and/or other elements of a frame format associated with wavelength channels 105. In those cases, one or more of the packet labels and/or other elements of a frame format associated with optical signal 2312 can be  
5 subjected to one or more optical-to-electrical and/or electrical-to-optical conversions.

In this particular embodiment, switching element 2300 comprises a ring network architecture where each  
10 wavelength associated with optical signal 2312 has its own virtual ring. Although this example implements a ring network architecture, any other network architecture, such as a star or mesh architecture, can be implemented without departing from the scope of the  
15 present disclosure. Implementing a ring network architecture within switching element 2300 can result in a minimization and/or avoidance of contention within backplane 2302. In some cases, minimizing and/or avoiding contention can reduce the need for buffering  
20 within switching element 2300. Minimizing and/or avoiding contention can result in a reduction in the congestion associated with an optical signal wavelength. In addition, the inherent symmetry of the ring network can operate to form a natural basis for fairness  
25 guarantee mechanisms. Moreover, at relatively higher transmission rates, the ring network architecture allows for the synchronous transmission of packet data having relatively simple protocol.

In operation, the ring network architecture  
30 associated with backplane 2302 can enable core router nodes 2304a-2304d and/or management node 2306 to implement relatively simple routing protocols. For example, in the "off-ramp," core router nodes 2304 can

check to see if the local address matches the incoming packet label associated with optical signal 2312, in which case the frame is down-loaded. In addition, for bandwidth-on-availability users, an empty slot algorithm  
5 can be employed. In such a case, a bandwidth-on-demand user waits for one of core router nodes 2304 to receive an empty packet data, in which case the empty packet data is replaced by a waiting packet data contained within a frame format. In various embodiments, if empty packets  
10 are required within switching element 2300, then management node 2306 can operate to insert empty packets into backplane 2302. In an alternative embodiment, switching element 2300 can implement a Quality of Service (QoS) routing protocol in which case higher QoS traffic  
15 can be placed on wavelengths that have a lighter loading.

FIGURE 24 is a block diagram of one embodiment of an electro-optic core router node 2400. In this example, core router node 2400 includes an optical tap 2402 capable of de-coupling at least a fraction of an optical  
20 signal 2420 from an optical fiber 2403. The structure and function of optical tap 2402 can be substantially similar to the structure and function of optical tap 1802 of FIGURE 18. In various embodiments, the structure and function of optical signal 2420 can be substantially  
25 similar to wavelength channel 1705 of FIGURE 17. In this particular embodiment, optical signal 2420 implements a frame format that comprises one or more framing bits, a first packet label that precedes packet data, and a second packet label that follows the packet data.

30 Node 2400 also includes a receiver 2404 capable of receiving at least a portion of optical signal 2420 de-coupled from optical fiber 2403. Receiver 2404 can comprise any optical and/or electrical component -

including any hardware, software, and/or firmware - capable of identifying and capturing a packet label associated with optical signal 2420. In this example, receiver 2404 converts the received portion of optical  
5 signal 2420 to an electronic signal 2405 representing the packet label and communicates electronic signal 2405 to a controller 2406. Controller 2406 can comprise any device capable of comparing electronic signal 2405 to a local address associated with node 2400. In various  
10 embodiments, controller 2406 may be capable of processing electronic signal 2405 at a processing speed of, for example, at least 10 Gb/s or at least 40 Gb/s.

In this particular embodiment, controller 2406 communicates a control signal 2407 to a packet switch  
15 2408. Packet switch 2408 may comprise any optical and/or electrical component - including any hardware, software, and/or firmware - capable of processing, dropping, and/or adding one or more packet data from node 2400. In various embodiments, packet switch 2408 can comprise one  
20 or more semiconductor optical amplifiers (SOA) capable of functioning as an optical switch. In other embodiments, a plurality of packet switches can be formed on a single semiconductor substrate. In some cases, optical signal 2420 may comprise a time guard band capable of allowing  
25 packet switch 2408 time to process and switch optical signal 2420 before processing a subsequently received optical signal.

In this example, packet switch 2408 receives optical signal 2420 from a delay line 2412. Delay line 2412 may  
30 comprise, for example, a sufficient length of optical fiber capable of compensating for any processing latency associated with receiver 2404 and/or controller 2406. In



some cases, delay line 2412 may operate as a first-in-first-out (FIFO) buffer having an appropriate length.

In this example, controller 2406 through control signal 2407 operates to control the state of packet switch 2408. For example, if the packet label associated with electronic signal 2405 matches the local address stored within controller 2406, then controller 2406 operates to cause packet switch 2408 to drop optical signal 2420 and add an empty packet. In addition, if the packet label associated with electronic signal 2405 is empty or comprises all '1' label and a new optical signal is waiting to be added at node 2400, then controller 2406 operates to cause packet switch 2408 to add the new optical signal. Moreover, if the packet label associated with electronic signal 2405 does not match the local address and/or is not empty, then controller 2406 operates to cause packet switch 2408 to pass optical signal 2420.

In this particular embodiment, node 2400 may further include an optical amplifier 2416 capable of at least partially compensating for any losses associated with optical signal 2420. In the illustrated embodiment, optical amplifier 2416 resides at the egress end of node 2400. Optical amplifier 2416 could also or alternatively reside at the ingress end of node 2400. Optical amplifier 2416 may comprise, for example, a Raman amplifier, a rare-earth doped amplifier, a semiconductor optical amplifier, or a combination of these or other amplifier types.

FIGURE 25 is a block diagram of one embodiment of a core router node 2500. In this example, core router node 2500 includes optical taps 2502a and 2502n capable of decoupling or removing at least a fraction of an optical

signal 2522 from an optical fiber 2503. The structure and function of optical taps 2502a and 2502n can be substantially similar to the structure and function of optical tap 1802 of FIGURE 18. Although this example  
5 implements two optical taps 2502a and 2502n, any number of optical taps can be used without departing from the scope of the present disclosure. In various embodiments, the structure and function of optical signal 2522 can be substantially similar to wavelength channel 1705 of  
10 FIGURE 17. In this particular embodiment, optical signal 2522 implements a frame format that comprises one or more framing bits, a first packet label that precedes packet data, a second packet label that follows the packet data, and a time guard band that separates optical signal 2522  
15 from a later received optical signal.

Node 2500 also includes a self-sync module 2504 capable of receiving at least a portion of optical signal 2522 de-coupled from optical fiber 2503. The structure and function of self-sync module 2504 can be  
20 substantially similar to the structure and function of self-sync module 1806 or module 1900 of FIGURES 18 and 19, respectively. In various embodiments, self-sync module 2504 can comprise one or more semiconductor optical amplifiers (SOA) capable of functioning as a  
25 fast-saturated slow recovery gain medium and/or an intensity discriminator. Implementing SOA components within self-sync module 2504 advantageously enables at least portions of self-sync module 2504 to be amenable to semiconductor chip level integration.

30 In this particular embodiment, self-sync module 2504 synchronizes the first bit associated with a packet label of optical signal 2522 and communicates a synchronized signal 2505 to a local address generator 2506. Generator

2506 may comprise any optical and/or electrical component - including any hardware, software, and/or firmware - capable of processing, replicating, storing and/or creating an empty header packet label and a local address  
5 packet label. In this example, generator 2506 operates to replicate synchronized signal 2505, one for each bit in the packet label associated with optical signal 2522. For example, if the packet label comprises six bits, then generator 2506 replicates synchronized signal 2505 six  
10 times. In various embodiments, generator can comprise a replication module that is substantially similar in structure and function to replication module 2004 of FIGURE 20.

In this example, generator 2506 also operates to  
15 convert the replicated bits into an empty header packet label comprising an all '1' label and to communicate a generator signal 2507 to all-optical header processor 2518. Generator signal 2507 can comprises an empty header packet label and a local address packet label  
20 capable of being used by processor 2518. Processor 2518 can comprise, for example, any optical and/or electrical components, including any hardware, software, or firmware, capable of comparing generator signal 2507 to a packet label associated with optical signal 2522. In  
25 various embodiments, processor 2518 can comprise one or more SOAs capable of functioning as an optical logic gate.

Node 2500 may also include a controller 2520 capable of receiving a processor signal 2519 and generating a  
30 control signal 2521 capable of controlling the state of a packet switch 2508. In various embodiments, processor 2518 and controller 2520 may be capable of processing generator signal 2507 at a processing speed of, for

example, at least 10 Gb/s, at least 40 Gb/s, at least 100 Gb/s, or at least 160 Gb/s. In this particular embodiment, controller 2520 communicates a control signal 2521 to packet switch 2508. In various embodiments, the  
5 structure and function of packet switch 2508 can be substantially similar to the structure and function of packet switch 2408 of FIGURE 24. In some cases, optical signal 2522 may comprise a time guard band capable of allowing packet switch 2508 time to process and switch  
10 optical signal 2522 before processing a subsequently received optical signal.

In this example, packet switch 2508 receives optical signal 2522 from a delay line 2512. Delay line 2512 may comprise, for example, a sufficient length of optical  
15 fiber capable of compensating for any processing latency associated with module 2504, generator 2506, processor 2518, and/or controller 2520. In some cases, delay line 2512 may operate as a first-in-first-out (FIFO) buffer having an appropriate length.

20 In this example, processor 2518 through controller 2520 operates to control the state of packet switch 2508. For example, if the packet label associated with optical signal 2522 matches the local address communicated by signal 2507, then processor 2518 operates to cause packet  
25 switch 2508 to drop optical signal 2522 and add an empty packet. In addition, if the packet label associated with optical signal 2522 is empty or comprises all '1' label and a new optical signal is waiting to be added at node 2500, then processor 2518 operates to cause packet switch  
30 2508 to add the new optical signal. Moreover, if the packet label associated with optical signal 2522 does not match the local address and/or is not empty, then

processor 2518 operates to cause packet switch 2508 to pass optical signal 2522.

In this particular embodiment, node 2500 may further includes an optical amplifier 2516 capable of at least partially compensate for any losses associated with optical signal 2522. In the illustrated embodiment, optical amplifier 2516 resides at the egress end of node 2500. Optical amplifier 2516 could also or alternatively reside at the ingress end of node 2500. Optical amplifier 916 may comprise, for example, a Raman amplifier, a rare-earth doped amplifier, a semiconductor optical amplifier, or a combination of these or other amplifier types.

FIGURE 26 is a block diagram of one embodiment of an all-optical header processing device 2600. In various embodiments, the structure and function of header processing device 2600 can be substantially similar to the structure and function of processor 2518 of FIGURE 25. In this example, header processing device 2600 includes an inverter 2604 capable of inverting a packet label associated with an optical signal 2602. The structure and function of optical signal 2602 can be substantially similar to the structure and function of optical signal 2522 of FIGURE 25. In this particular embodiment, optical signal 2602 implements a frame format that comprises at least one packet label.

Header processing device 2600 also includes an all-optical XOR gate 2608 capable of comparing the inverted packet label associated with optical signal 2602 to a generator signal 2610. In various embodiments, the structure and function of XOR gate 2608 can be substantially similar to the structure and function of XOR gate 2200 of FIGURE 22A or XOR gate 2014 of FIGURE

20. The structure and function of generator signal 2610 can be substantially similar to the structure and function of generator signal 2507 of FIGURE 25. In this particular embodiment, generator signal 2610 comprises a  
5 local address packet label and an empty header label.

In operation, inverter 2604 and XOR gate 2608 implement threshold detection schemes capable of determining if an output level associated with the inverted packet label falls below a threshold level. In  
10 this example, if the inverted packet label comprises an empty header packet label, then a first threshold detector 2606 operates to output an empty packet label. Otherwise, XOR gate 2608 operates to compare the inverted packet label associated with optical signal 2602 to a  
15 local address associated with a core router node. If the inverted packet label associated with optical signal 2602 matches the local address packet label, then XOR gate 2608 outputs an all zero packet label and a second threshold detector 2612 generates a control signal. In  
20 that example, the control signal is capable of being used by a packet switch to add and/or drop an associated optical signal.

FIGURE 27 is a block diagram of one embodiment of a packet switch 2700. In various embodiments, the  
25 structure and function of packet switch 2700 can be substantially similar to the structure and function of packet switch 2508 of FIGURE 25. In this example, packet switch 2700 includes a divider 2702 and a combiner 2704 capable of separating, dividing, combining, coupling, and/or routing an optical signal 2708. In one non-  
30 limiting example, divider 2702 may comprise a 50/50 power divider and combiner 2704 may comprise a 50/50 power combiner.

In various embodiments, the structure and function of optical signal 2708 can be substantially similar to wavelength channel 1705 of FIGURE 17. In this particular embodiment, optical signal 2708 comprises a guard band  
5 that separates optical signal 2708 from a later received optical signal. In some cases, the guard band can comprise, for example, at least 0.5 nano-seconds or at least one (1) nano-second.

Packet switch 2700 also includes at least a first  
10 SOA 2706a, a second SOA 2706b, and a third SOA 2706c. As one non-limiting example, SOA's 2706 can comprise an InGaAsP semiconductor optical amplifier. In this example, SOA's 2706 operate in either gain or absorption enabling on/off functionality in each arm of packet  
15 switch 2700. Implementing SOA components within packet switch 2700 advantageously enables packet switch 2700 to be amenable to semiconductor chip level integration. In one particular embodiment, a plurality of packet switches can be formed on a single semiconductor chip using  
20 standard semiconductor processing techniques. In those cases, the semiconductor substrate may comprise, for example, silicon, poly-silicon, indium phosphide, gallium arsenide, or any other suitable substrate material or combination of substrate materials.

25 In this example, packet switch 2700 operates to control the state and functionality of SOA's 2706. For example, if optical signal 2708 is to pass through packet switch 2700, then SOA 2706a is turned on and SOA 2706b is turned off. In addition, if optical signal 2708 is to be  
30 removed or dropped from packet switch 2700, then SOA 2706a is turned off and SOA 2706b is turned on. Moreover, if a new optical signal is to be added to packet switch 2700, then SOA 2706c is turned on and SOA

2706a is turned off. Implementing a guard band between optical signal 2708 and a later received optical signal advantageously allows packet switch 2708 to accommodate the switching time associated with each of SOAs 2706. In  
5 other embodiments, SOAs 2706 can comprise a polarization dependent loss of, for example, no more than three (3) dB, two (2) dB, or one-half (0.5) dB.

FIGURE 28 is a block diagram of one embodiment of a management node 2800. In some embodiments, management  
10 node 2800 operates to initialize a switching element at start-up and to perform clean-up of unwanted packets. In this example, management node 2800 includes an optical tap 2802 capable of de-coupling at least a fraction of an optical signal 2820 from an optical fiber 2803. The  
15 structure and function of optical tap 2802 can be substantially similar to the structure and function of optical tap 1802 of FIGURE 18. In various embodiments, the structure and function of optical signal 2820 can be substantially similar to wavelength channel 1705 of  
20 FIGURE 17. In this particular embodiment, optical signal 2820 implements a frame format that comprises one or more framing bits, a first packet label that precedes packet data, a second packet label that follows the packet data, and a time guard band that separates optical signal 2820  
25 from a later received optical signal.

Node 2800 also includes a self-sync module 2804 capable of receiving at least a portion of optical signal 2820 de-coupled from optical fiber 2803. The structure and function of self-sync module 2804 can be  
30 substantially similar to the structure and function of self-sync module 1806 or module 1900 of FIGURES 18 and 19, respectively. In various embodiments, self-sync module 2804 can comprise one or more semiconductor



optical amplifiers (SOA) capable of functioning as a fast-saturated slow recovery gain medium and/or an intensity discriminator. Implementing SOA components within self-sync module 2804 advantageously enables at least portions of self-sync module 2804 to be amenable to semiconductor chip level integration.

In this particular embodiment, self-sync module 2804 synchronizes the first bit associated with a packet label of optical signal 2820 and communicates a synchronized signal 2805a to an empty packet generator 2806. Generator 2806 may comprise any optical and/or electrical component - including any hardware, software, and/or firmware - capable of processing, replicating, and/or creating an empty header packet label. In this example, generator 2806 operates to replicate synchronized signal 2805, once for each bit in the packet label associated with optical signal 2820. In various embodiments, generator 2806 can comprise a replication module that is substantially similar in structure and function to replication module 2004 of FIGURE 20.

In this example, generator 2806 also operates to convert the replicated bits into an empty header packet label comprising an all '1' label and to communicate a generator signal 2807 to a packet switch 2808. In various embodiments, the structure and function of packet switch 2808 can be substantially similar to the structure and function of packet switch 2508 of FIGURE 25. In some cases, optical signal 2820 may comprise a time guard band capable of allowing packet switch 2808 time to process and switch optical signal 2820 before processing a subsequently received optical signal.

Self-sync module 2804 also operates to communicate a synchronized signal 2805b to a scavenger bit module 2810.

Scavenger bit module 2810 may comprise any optical and/or electrical component - including any hardware, software, and/or firmware - capable of processing, comparing, and/or updating a monitor bit associated with optical  
5 signal 2820. In this particular embodiment, module 2810 communicates a scavenger bit signal 2811 to a controller 2814. Controller 2814 can comprise any optical and/or electrical components, including any hardware, software, or firmware, capable of controlling the state of packet  
10 switch 2808. In this particular embodiment, controller communicates a control signal 2815 to packet switch 2808. In various embodiments, module 2810 and controller 2814 may be capable of processing optical signal 2820 at a processing speed of, for example, at least 10 Gb/s, at  
15 least 40 Gb/s, at least 100 Gb/s, or at least 160 Gb/s.

In this example, packet switch 2808 receives optical signal 2820 from a delay line 2812. Delay line 2812 may comprise, for example, a sufficient length of optical fiber capable of compensating for any processing latency  
20 associated with node 2800. In some cases, delay line 2812 may operate as a first-in-first-out (FIFO) buffer having an appropriate length.

In this example, module 2810 and controller 2814 operate to control the state of packet switch 2808. For  
25 example, if management node 2800 receives a packet label with an illegal destination address or a packet label whose destination address is inoperative, then packet switch 2808 operates to drop the inoperative or illegal signal. In addition, if management node 2800 detects  
30 that an optical signal has traversed node 2800 more than once, then node 2800 assumes the intended destination is not on-line and drops the optical signal. Management node 2800 minimizes recirculation of optical signals by

setting the monitor bit associated with the optical signal to '1' the first time the optical signal traverses node 2800. If management node 2800 receives a monitor bit comprising a '1', then it assumes that the  
5 destination address is not on-line. Implementing a minimization scheme can minimize congestion within an optical switching element.

In this particular embodiment, node 2800 may further include an optical amplifier 2816 capable of at least  
10 partially compensating for any losses associated with optical signal 2820. In the illustrated embodiment, optical amplifier 2816 resides at the egress end of node 2800. Optical amplifier 2816 could also or alternatively reside at the ingress end of node 2800. Optical  
15 amplifier 2816 may comprise, for example, a Raman amplifier, a rare-earth doped amplifier, a semiconductor optical amplifier, or a combination of these or other amplifier types.

FIGURE 29 is a block diagram of one embodiment of an  
20 all-optical error check device 2900. In various embodiments, error check device 2900 can be accessible to core router node 2400 of FIGURE 24, core router node 2500 of FIGURE 25, maintenance node 2800 of FIGURE 28, or to any other optical device. In one particular embodiment,  
25 error check device 2900 can be accessible to edge router 2310 of FIGURE 23. In this example, error check device 2900 includes an optical splitter 2902 capable of separating a first packet label from an optical signal 2910 received by splitter 2902. In various embodiments,  
30 the structure and function of optical signal 2910 can be substantially similar to the structure and function of optical signal 1703 or wavelength channel 1705 of FIGURE 17. In this particular embodiment, optical signal 2910

implements a frame format that comprises a first packet label that precedes packet data and a second packet label that follows the packet data. Splitter 2902 can comprise any optical or electrical components capable of  
5 identifying, de-coupling, and/or capturing at least a fraction of a first packet label associated with optical signal 2910.

Error check device 2900 also includes a delay device 2904 capable of sufficiently delaying optical signal 2910  
10 to allow an all-optical XOR gate 2906 to compare the first packet label and the second packet label associated with optical signal 2910. As a non-limiting example, delay device 2904 can comprise a sufficient length of optical fiber. In various embodiments, the structure and  
15 function of XOR gate 2906 can be substantially similar to the structure and function of XOR gate 2200 of FIGURE 22A or XOR gate 2014 of FIGURE 20. In this particular embodiment, XOR gate 2906 comprises an SOA-MZI.

In operation, XOR gate 2906 implements threshold  
20 detection scheme capable of determining if an output level associated with optical signal 2910 falls below a threshold level. For example, if the first packet label and the second packet label match, then an all '0' output is achieved and optical signal 2910 proceeds to its  
25 destination address. However, if the first packet label and the second packet label do not match, then threshold detector 2908 generates a warning signal that the data may be corrupted.

FIGURE 30 is a block diagram of one embodiment of an  
30 output interface 3000. In various embodiments, output interface 3000 may be capable of preparing an optical signal 3003 for communication over a communication link 3006 and/or to minimize contention within communication

link 3006. Although this example illustrates communication link 3006 as being directly coupled to output interface 3000, communication link 3006 can be indirectly coupled to output interface 3000 without  
5 departing from the scope of the present disclosure. The structure and function of communication link 3006 can be substantially similar to the structure and function of communication link 1702 or communication link 1716 of  
10 FIGURE 17. In various embodiments, the structure and function of optical signal 3003 can be substantially similar to the structure and function of optical signal 1703, optical signal 1715, optical signal 1715, wavelength channel 1705, or wavelength channel 1713 of  
FIGURE 17.

15 In this example, output interface 3000 includes a regeneration module 3002 capable of 3R regeneration and/or wavelength conversion. In one non-limiting example, regeneration module 3002 can comprise, for example, an SOA-MZI capable of 3R regeneration and/or  
20 wavelength conversion. In various embodiments, the structure and function of regeneration module 3002 can be substantially similar to the structure and function of logic gates 2200, 2225, 2250, or 2275 of FIGURES 22A-22D. Implementing SOA components within regeneration module  
25 3002 can, in some cases, allow regeneration module 3002 and/or output interface 3000 to be amenable to semiconductor chip level integration. In one particular embodiment, a plurality of regeneration modules 3002 and/or output interfaces 3000 can be formed on a common  
30 semiconductor chip using standard semiconductor processing techniques.

Output interface 3000 also includes a synchronized light source 3004 capable of generating a pulsed and/or

modulated output signal 3005. Synchronized light source 3004 can comprise, for example, a tunable light source, a semiconductor laser, a laser diode, a cladding pump fiber laser, or any combination of these or any other light source. In various embodiments, output signal 3005 is capable of being synchronized to optical signal 3003 using a phase locked loop. The synchronized light source operates to provide a re-timing function. In some embodiments, output signal 3005 can comprise the same wavelength as optical signal 3003 or can comprise a different wavelength than data optical signal 3003. In some cases, implementing a different output signal 3005 wavelength can result in a wavelength shift and possibly minimize or avoid contention within a network or optical device coupled to output interface 3000.

FIGURE 31 is a block diagram of an optical communication network 3100 capable of implementing one or more core optical data routers 3106. Network 3100 may comprise any wireless network, wireline network, or combination of wireless and wireline networks capable of supporting communication between network elements using ground-based and/or space-based components. For example, network 104 may comprise a data network, an optical network, a public switched telephone network (PSTN), an integrated services digital network (ISDN), a local area network (LAN), a wide area network (WAN), a metropolitan area network (MAN), all or a portion of the global computer network known as the Internet, and/or other communication systems or combination of communication systems at one or more locations. In various embodiments, network 3100 may be capable of an aggregate capacity of, for example, at least 5 Tb/s, at least 10 Tb/s, at least 25 Tb/s, at least 50 Tb/s, or at least 100

Tb/s. In other embodiments, network 3100 may be capable of transmitting packet data at transmission rates of, for example, at least 10 Gb/s, at least 40 Gb/s, at least 100 Gb/s, or at least 160 Gb/s.

5 In this example, communication network 3100 includes access nodes 3102a-3102f. Access nodes 3102 may comprise any optical and/or electrical components - including any hardware, software, and/or firmware - capable of receiving and/or converting any analog and/or digital  
10 signals in an electrical or optical format to an optical format for transmission through communication network 3100. In this particular embodiment, access nodes 3102 are capable of providing electronic and/or optical buffering until an optical signal 3110 is ready to be  
15 communicated through communication network 3100. In addition, access nodes 3102, in some cases, may be capable of placing packet data in a frame format and/or compressing the frame. When the packet is unloaded, access nodes 3102 can operate to decompress the frame,  
20 remove the packet data from the frame format and/or generate an error warning.

Access nodes 3102a-3102f, in some cases, may also examine header data of signals received from communication links 3104a-3104f to identify at least an  
25 initial signal path through all or part of communication network 3100. Accordingly, access nodes 3102a-3102h may attach a destination address to a packet and frame or encapsulate the data for transmission across communication network 3100. Access nodes receiving  
30 encapsulated packets at egress points from communication network 3100 can remove the framing that was attached at the ingress edge node and facilitate transmission of the signal toward a destination external network element.

For example, where the signal received at an ingress access node was an electrical signal, an egress access node can convert the optical signal received from communication network 3100 to an electrical signal for  
5 transmission toward a destination external network element in an electrical format.

Communication links 3104a-3104f may comprise any communications medium capable of assisting in the communication of analog and/or digital signals.  
10 Communications links 3104 may, for example, comprise a twisted-pair copper telephone line, a fiber optic line, a Digital Subscriber Line (DSL), a wireless link, a USB bus, a PCI bus, an ethernet interface, or any other suitable interface operable to assist in the  
15 communication of information to and/or from network 3100.

In this particular example, at least a portion of communication network 3100 includes an optical data network 3101 comprising core optical data routers 3106a-3106h. In this example, optical data network 3101  
20 comprises a transparent network in the sense that data network 3101 is not required to examine the contents and/or the format of the data in the packet. That is, the data portion of the packet can remain in an optical format from the ingress access node to the egress access  
25 node. In various embodiments, the structure and function of core optical data routers 3106 can be substantially similar to the structure and function of optical data router 1700 of FIGURE 17. Although this example includes eight core optical data routers, any number of core  
30 optical data routers can be used without departing from the scope of the present disclosure. In this particular embodiment, core optical data router 3106a-3106h can implement one or more semiconductor optical amplifier



(SOA) components. Implementing one or more SOA components within core optical data routers 3106 can, in some cases, allow at least a portion of these devices to be amenable to semiconductor chip level integration. In  
5 some embodiments, core optical data routers 3106 can implement one or more semiconductor optical amplifier Mach Zehnder Interferometer (SOA-MZI) based all-optical logic gates capable of 2R regeneration, 3R regeneration, and/or wavelength conversion.

10 In various embodiments, core optical data routers 3106a-3106h can operate to maintain the packet data associated with optical signals 3110 in the optical domain. That is, the packet data associated with optical signal 3110 is not subjected to an optical-to-electrical  
15 conversion within routers 3106a-3106h. In some cases, one or more of the packet labels and/or other elements of a frame format associated with optical signals 3110 can be subjected to one or more optical-to-electrical and/or electrical-to-optical conversions. In this particular  
20 embodiment, core optical data routers 3106a-3106h can also operate to minimize and/or avoid contention between packet data associated with one or more optical signals 3110 within optical network 3101. Minimizing and/or avoiding contention can result in a reduction in the  
25 congestion associated with an optical signal wavelength.

Optical signals 3110 may comprise a multiple wavelength optical signal. In one particular embodiment, optical signals 3110 can comprise at least 250 wavelength channels. In various embodiments, the structure and  
30 function of optical signals 3110 can be substantially similar to optical signal 1703 of FIGURE 17. In other embodiments, optical signal 3110 can implement one or more data formats, such as, polarization shift keying

(PLSK), pulse position modulation (PPM), Generalized Multi-Protocol Label Swapping (GMPLS), non-return to zero (NRZ), return to zero (RZ), differential phase shift key (DPSK), or a combination of these or other format types.

5        In this example, each core optical data router 3106 is coupled to another core optical data router through a communication link 3112. Communication link 3112 can comprise, for example, standard single mode fiber (SMF),  
dispersion-shifted fiber (DSF), non-zero  
10 dispersion-shifted fiber (NZDSF), dispersion compensating fiber (DCF), or another fiber type or combination of fiber types. In some embodiments, communication links 3112 can operate to couple core optical data routers 3106 to an edge router 3104.

15        In operation, each of access nodes 3102a-3102f communicates with one or more external network elements via communication links 3104a-3104f, respectively. Ingress access nodes of optical data network 3101 receive electrical and/or optical signals from communication  
20 links 3104a-3104f, convert any electrical signals received to optical signals, determine destination addresses associated with the signals, frame the signals appending the destination addresses to the signals (if not already framed), and route the optical signals toward  
25 an egress access node of communication network 3100. Optical signals 3110 traversing optical data network 3101 pass through one or more core optical data routers 3106. Each core optical data router 3106 routes optical signals 3110 toward the egress access node using one or more SOA  
30 components capable of implementing all-optical processing.

Although the present invention has been described in several embodiments, a myriad of changes, variations,

alterations, transformations, and modifications may be suggested to one skilled in the art, and it is intended that the present invention encompass such changes, variations, alterations, transformations, and  
5 modifications as falling within the spirit and scope of the appended claims.